

# MATLAB EXPO

## 2021

### Integrating AI into Model-Based Design

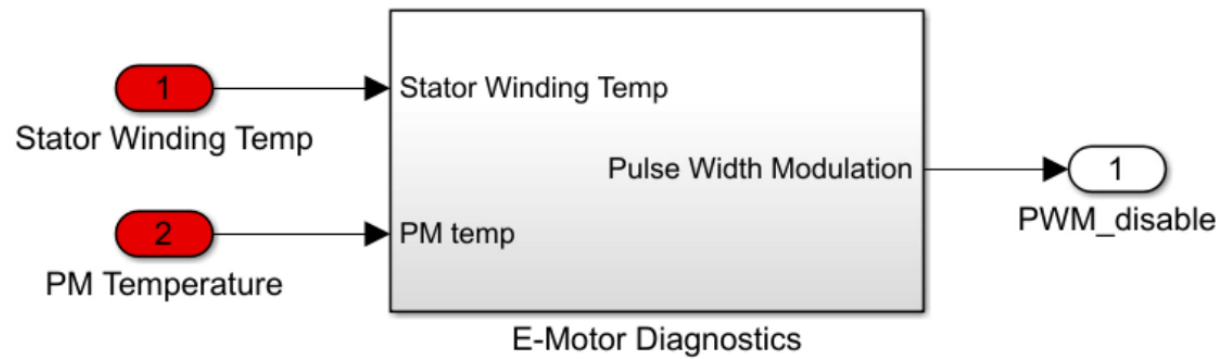
*Shyam Keshavmurthy and Javier Gazzarri*



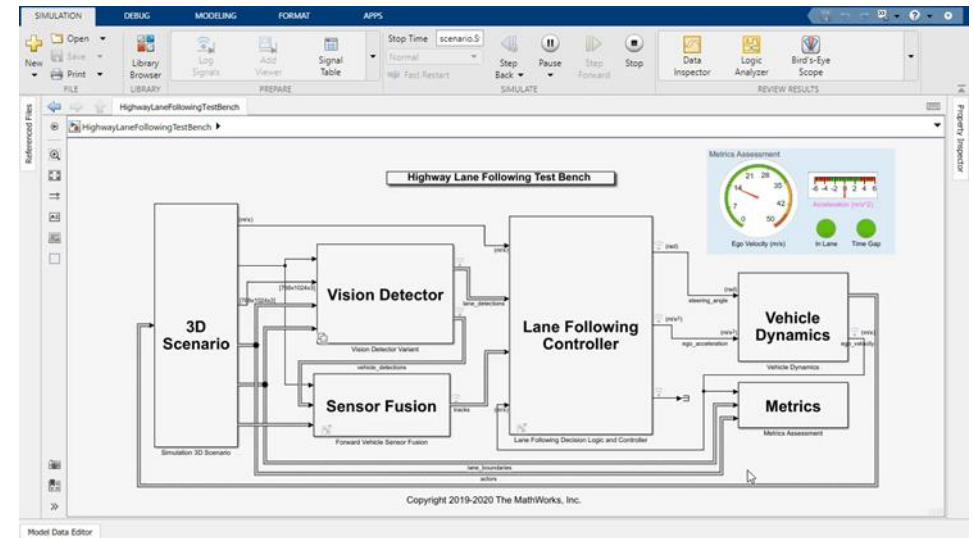




# Two Projects



Motor Diagnostic



Lane and vehicle detection

## Our Goal

# Integrate trained AI models into Simulink

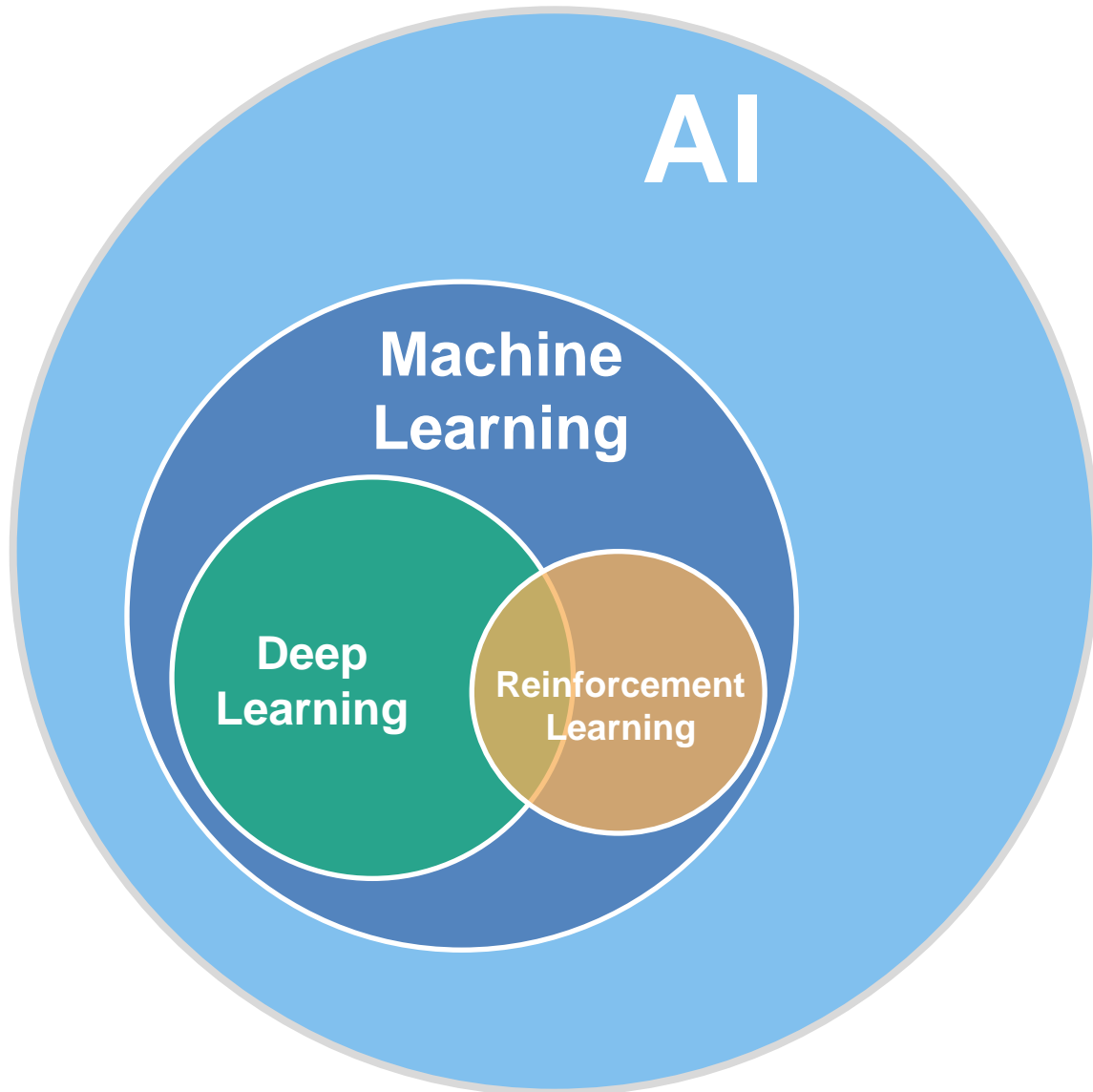
- Test design in simulation
- Code generation
- Import external AI models

## Poll questions

**What type of application are you considering with AI?**

- 1. Automated driving**
- 2. Robotics**
- 3. Powertrain**
- 4. Radar & wireless**
- 5. Other**
- 6. I haven't and am not planning to anytime soon.**

# Learning Algorithms Driving the AI Megatrend



Statistics and Machine Learning Toolbox

Deep Learning Toolbox

Reinforcement Learning Toolbox

# Increasing System Complexity

Model-Based Design and AI can help build complex systems

**System  
Requirements**

**System Functionality  
and Architecture**

**Subsystem  
Design**

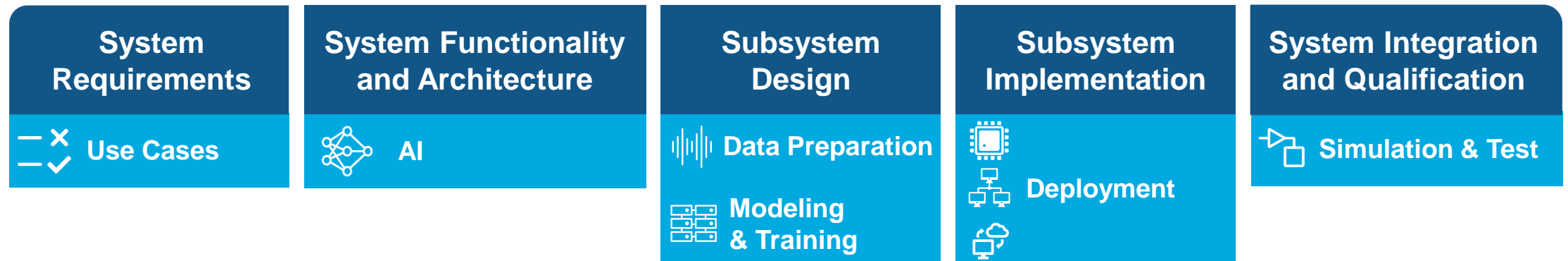
**Subsystem  
Implementation**

**System Integration  
and Qualification**



# Increasing System Complexity

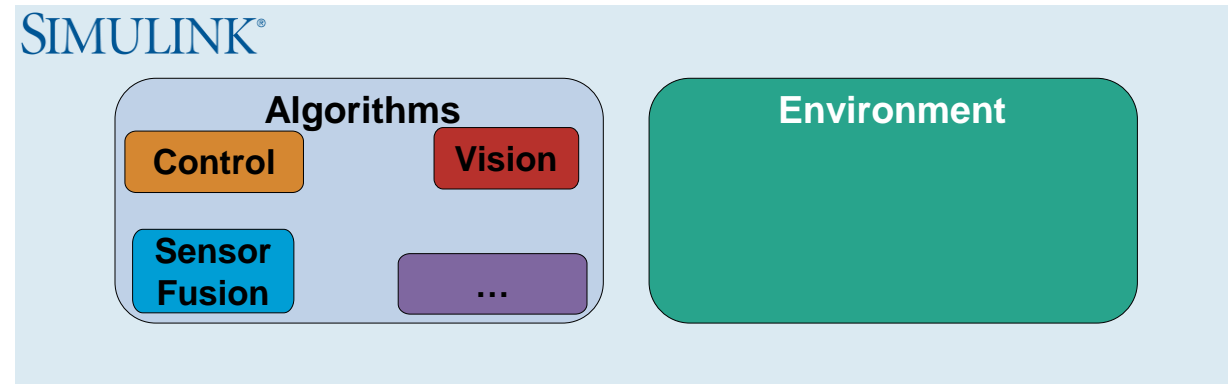
Model-Based Design and AI can help build complex systems



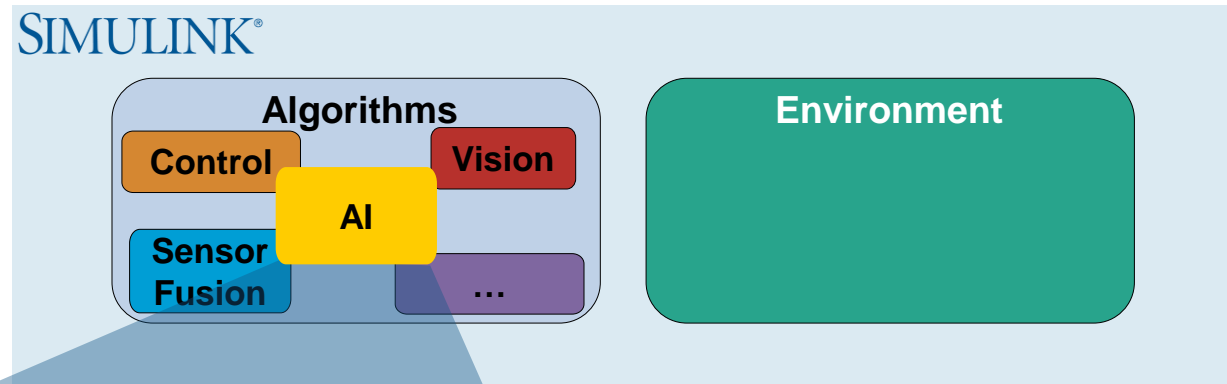
AI-driven system design workflow



# AI Models in Simulink



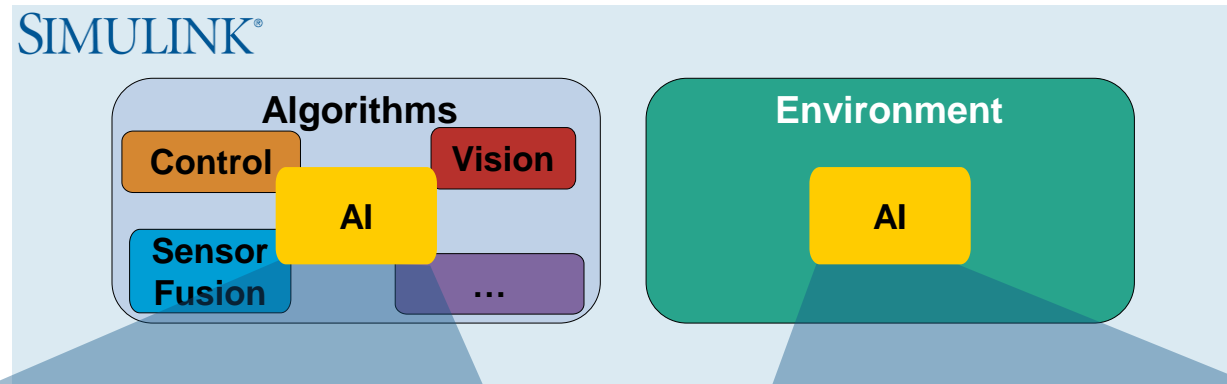
# AI Models in Simulink



## AI for algorithm development

- Simulate for system-level testing
- Verify system requirements
- Deploy to CPU/GPU/ECU/FPGA

# AI Models in Simulink



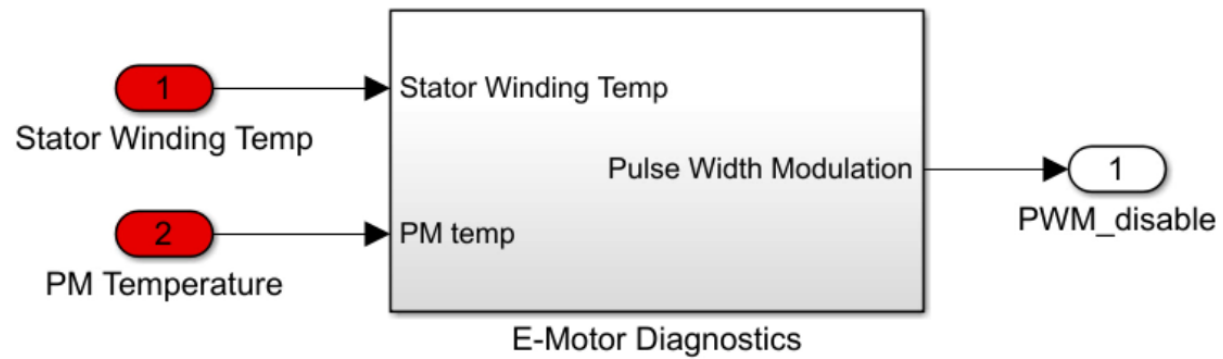
## AI for algorithm development

- Simulate for system-level testing
- Verify system requirements
- Deploy to CPU/GPU/ECU/FPGA

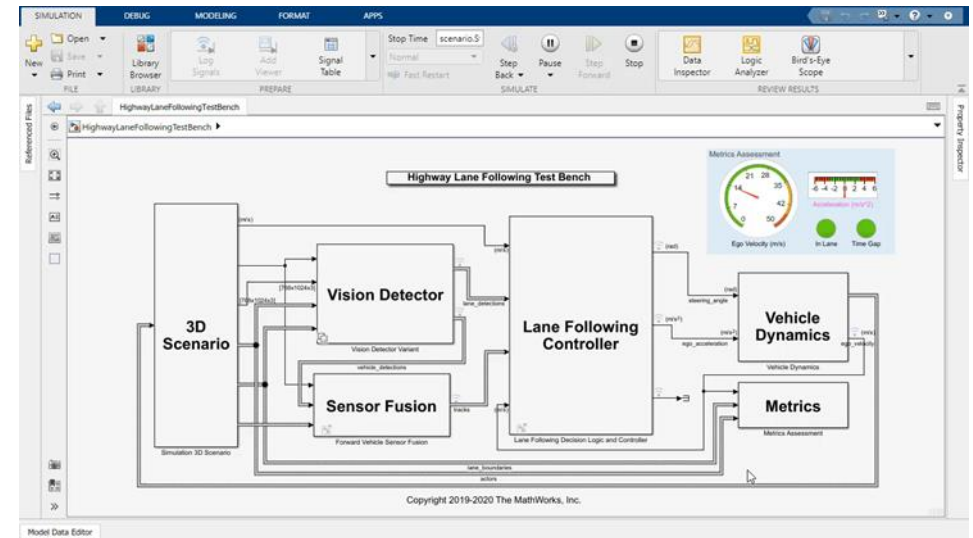
## AI for environment modeling

- Speed up high-fidelity model
- Reduce complexity - First principles → data driven
- Enable HIL tests
- Share component

# Case Study 1



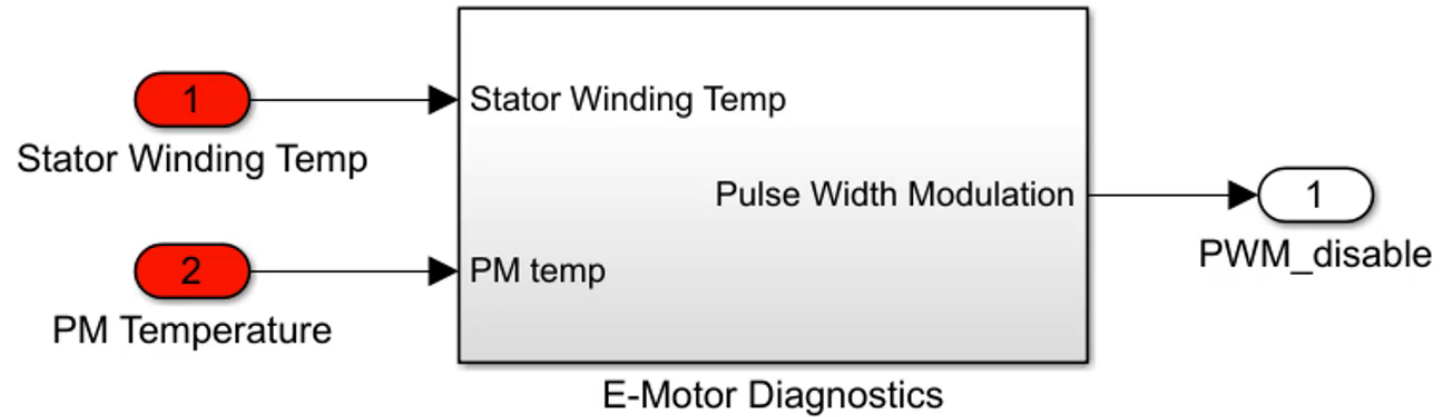
Motor Diagnostic



Lane and vehicle detection

# One Week Later

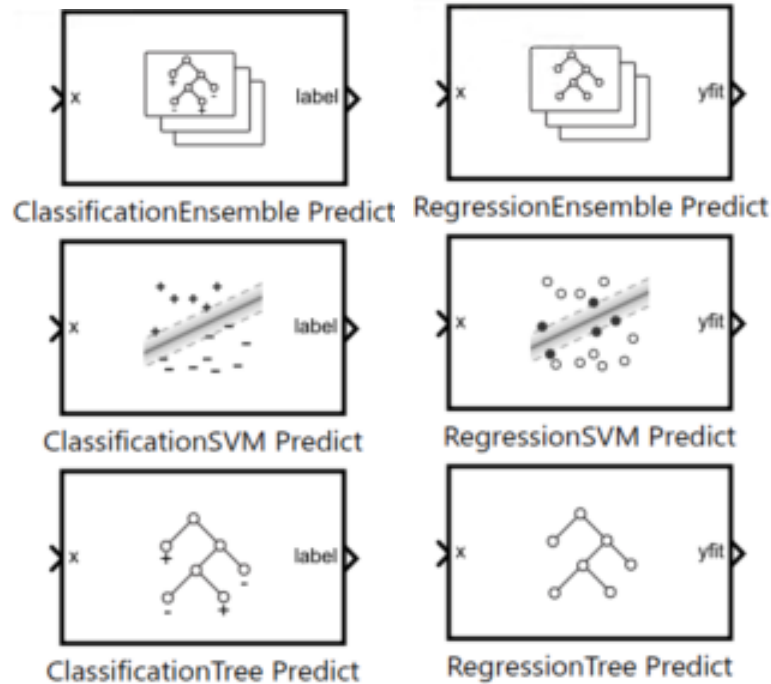
# Estimate Motor States with Machine Learning



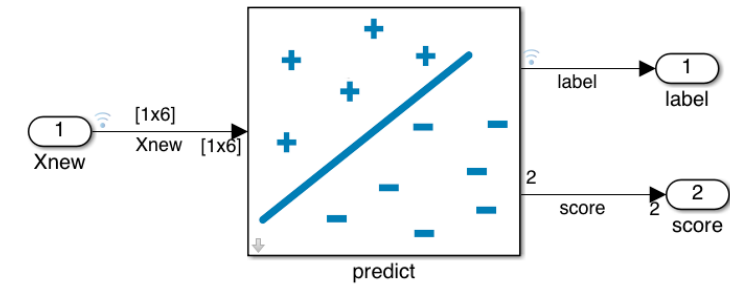
# How to Integrate Machine Learning?

## Built-in Machine Learning blocks

R2021a

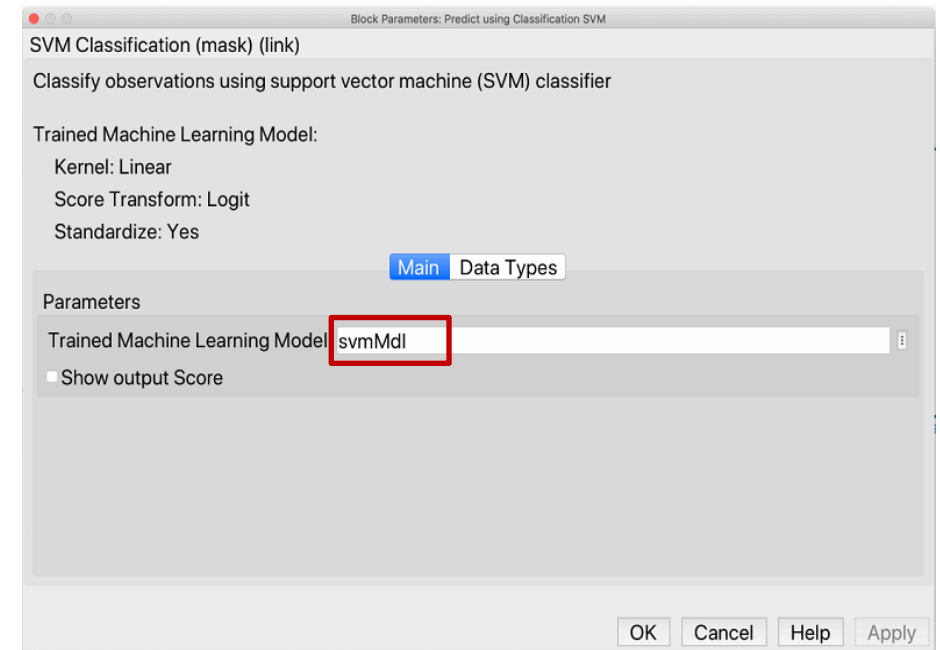


## Example: SVM Classification block



## MATLAB Function Blocks

- Preprocessing
- Feature Extraction
- Other models





HOME PLOTS APPS

Design App Get More Apps Install App Package App

Curve Fitting Optimization PID Tuner Analog Input Recorder Analog Output Generator Modbus Explorer CAN Explorer CAN FD Explorer System Identification Wireless Waveform Ge... Signal Analyzer Image Acquisition

FILE APPS

C:\Users\jgazzarr\OneDrive - MathWorks\Work\Seminars\EXPO2021

Command Window

Current Folder

```
fx >>
```

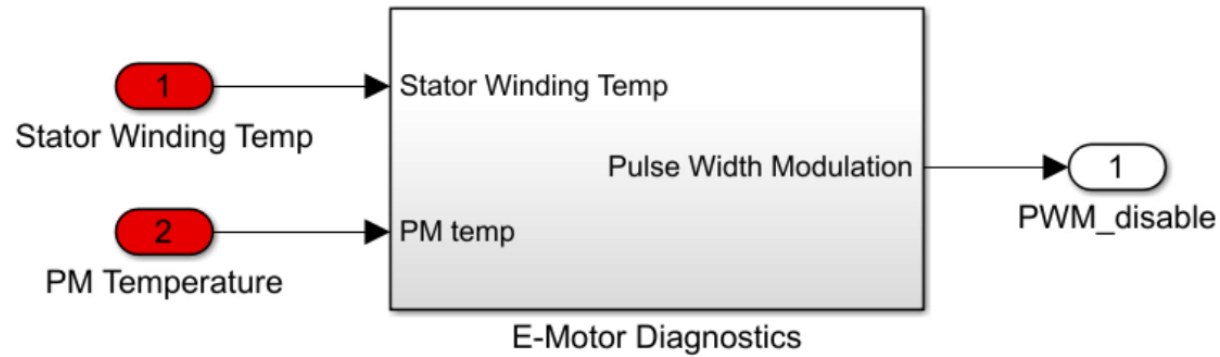
Workspace

Name	Value	Bytes
out	1x1 SimulationOutput	23037
trainedModel	1x1 struct	12453424

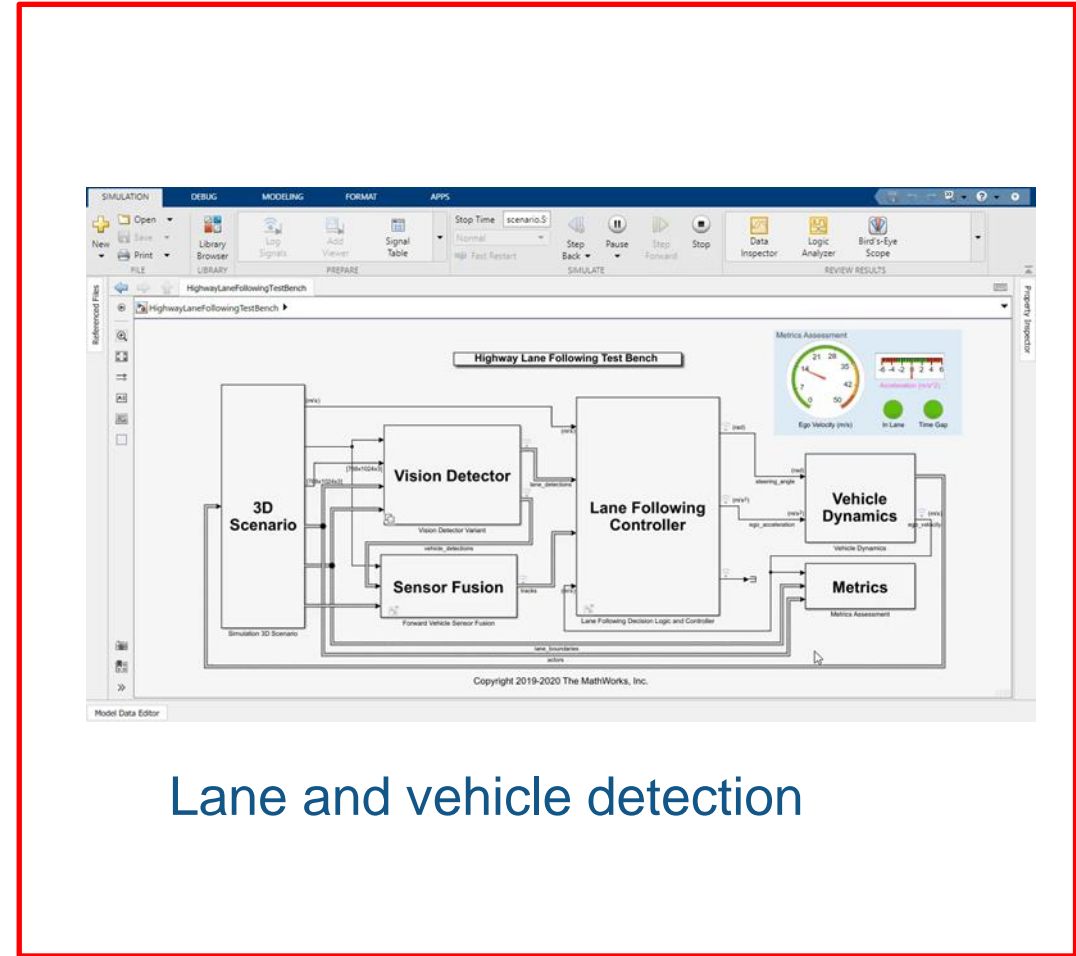
## Poll questions

- **How would you be interested in using AI?**
  1. **For algorithm development (algorithm that will be eventually deployed)**
  2. **For environment modeling (using reduced-order/surrogate modeling of the environment/plant)**

# Case Study 2

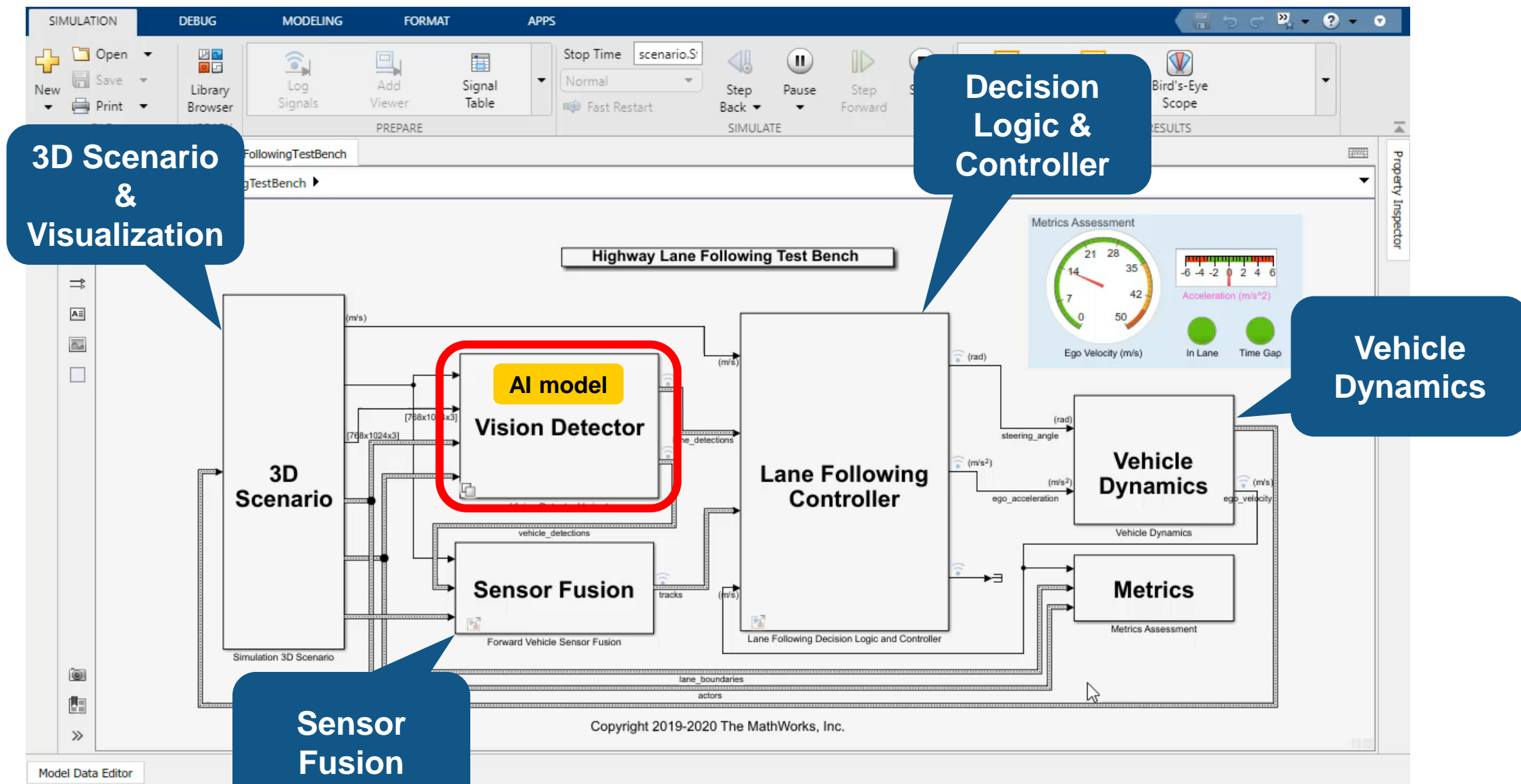


Motor Diagnostic




Lane and vehicle detection

# Highway Lane Following Model



# Deep Learning Networks in MATLAB/Simulink



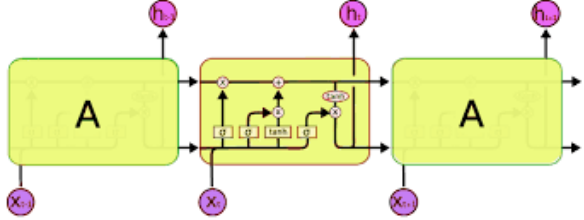
**Image Classification,  
Semantic Segmentation**

- ResNet
- Inception v3
- MobileNet v2
- GoogLeNet
- VGG



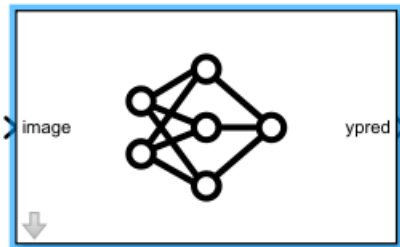
**Object Detectors**

- YOLO v2
- SSD

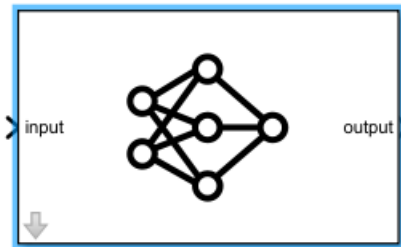


**Sequence Networks**

- LSTM
- BiLSTM



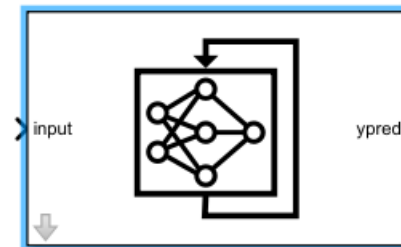
**IMAGE  
CLASSIFIER**



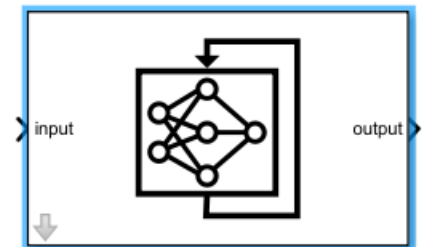
**PREDICT**



**MATLAB  
FUNCTION**

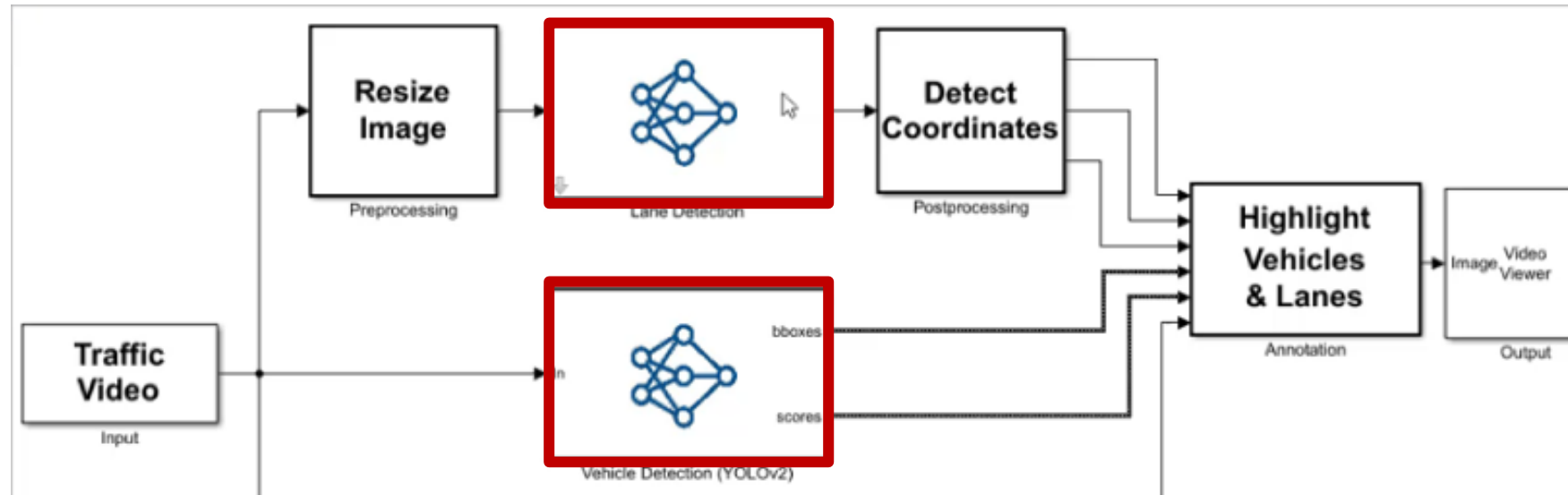
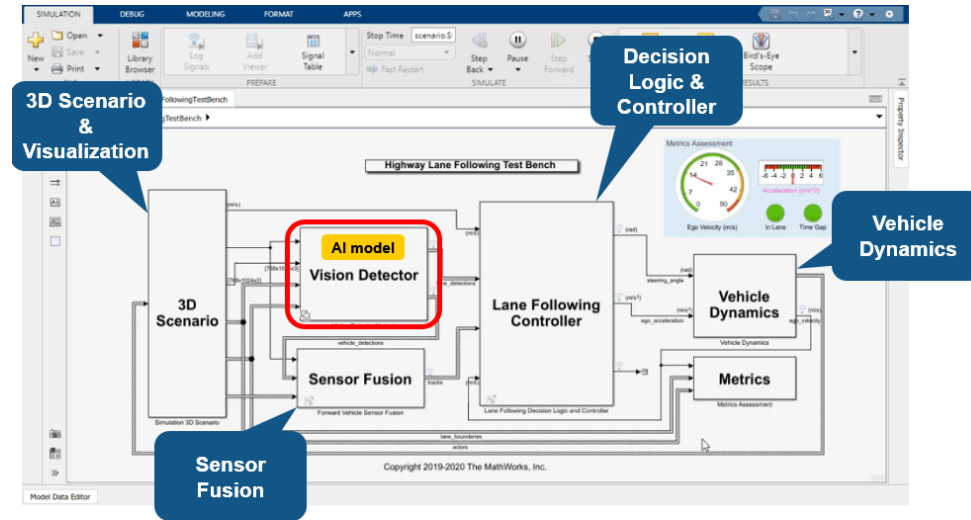


**STATEFUL  
CLASSIFY**

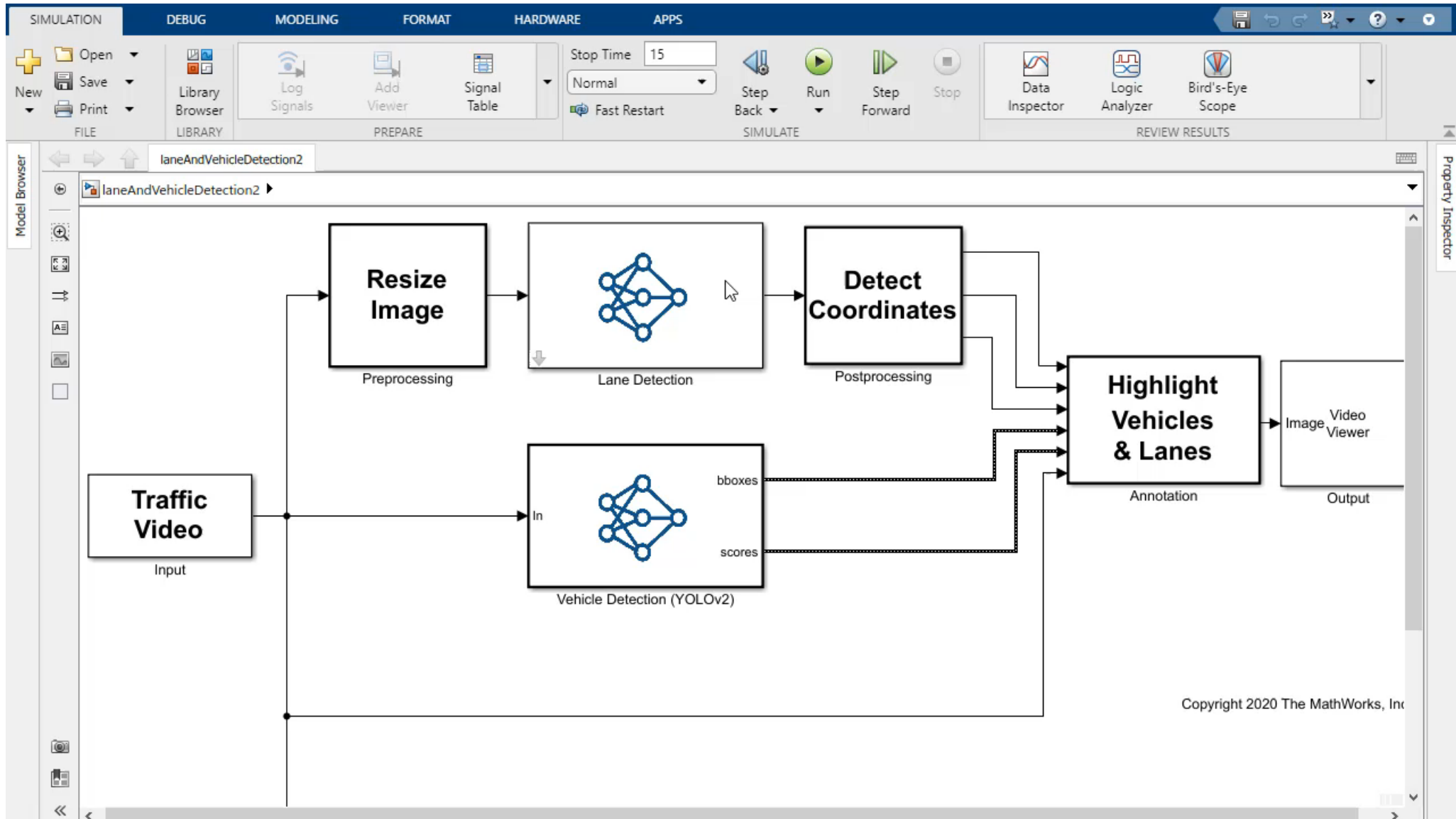


**STATEFUL  
PREDICT**

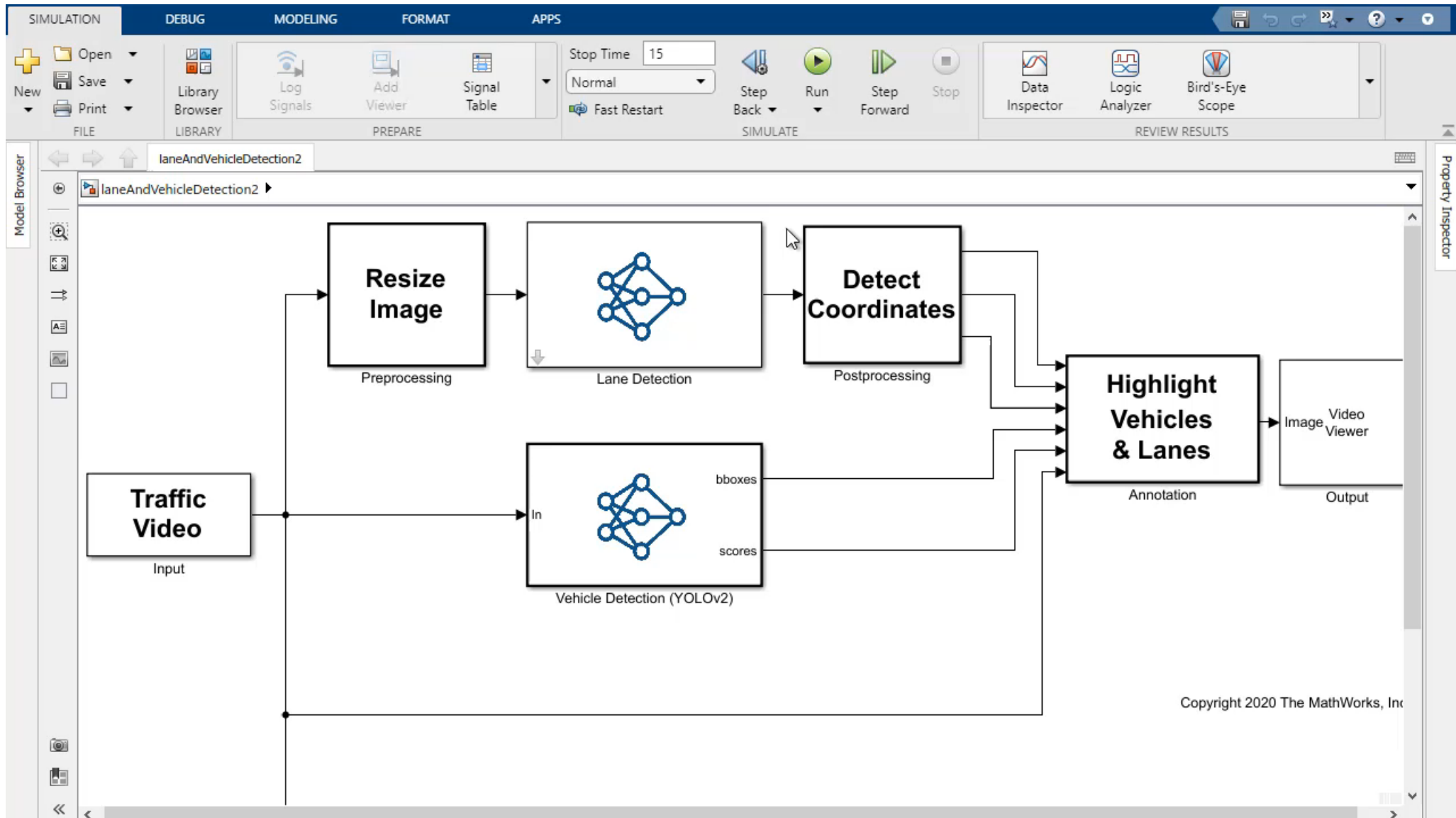
# AI Lane Following Model



# CPU Simulation

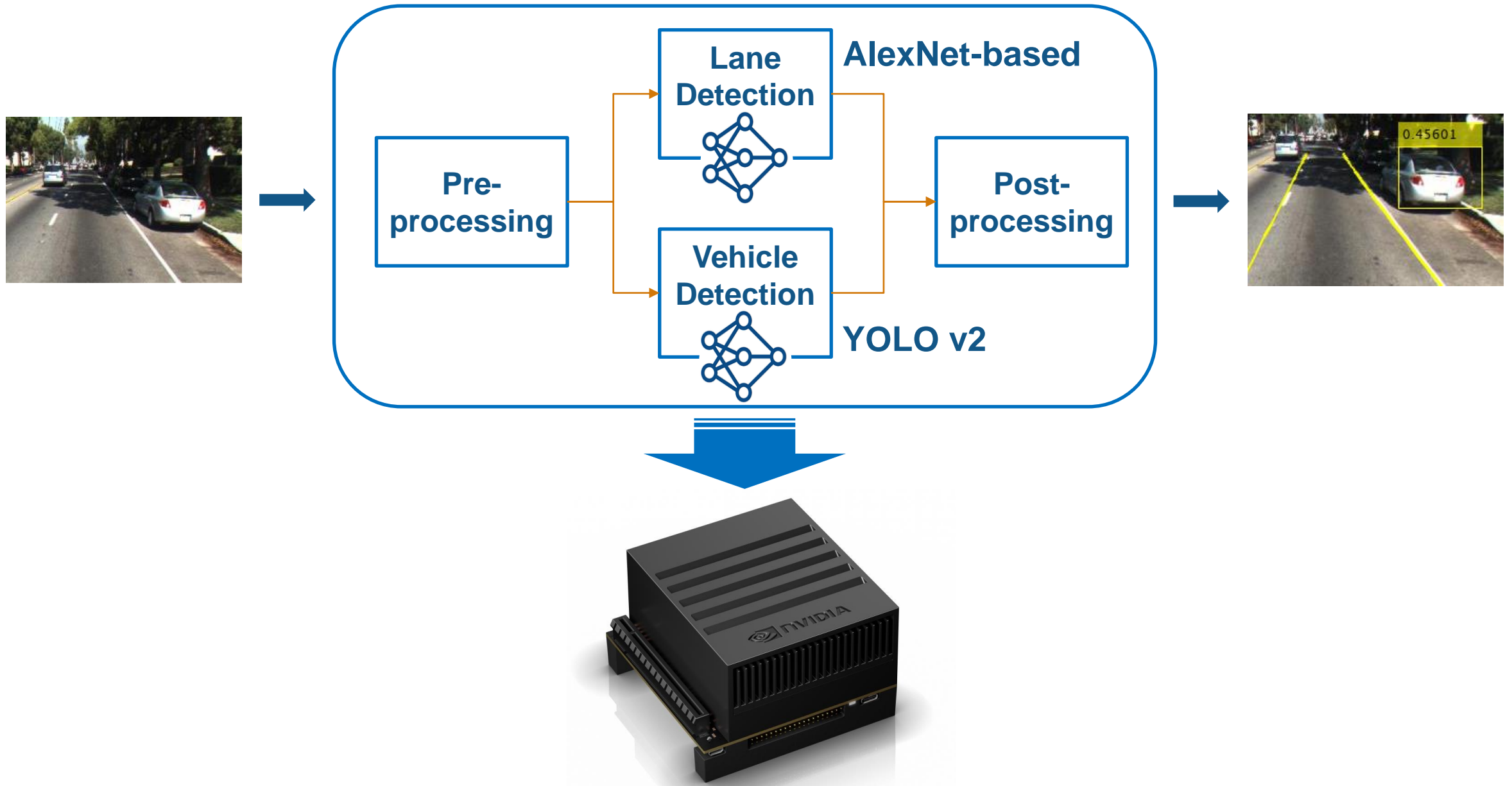


# GPU Simulation

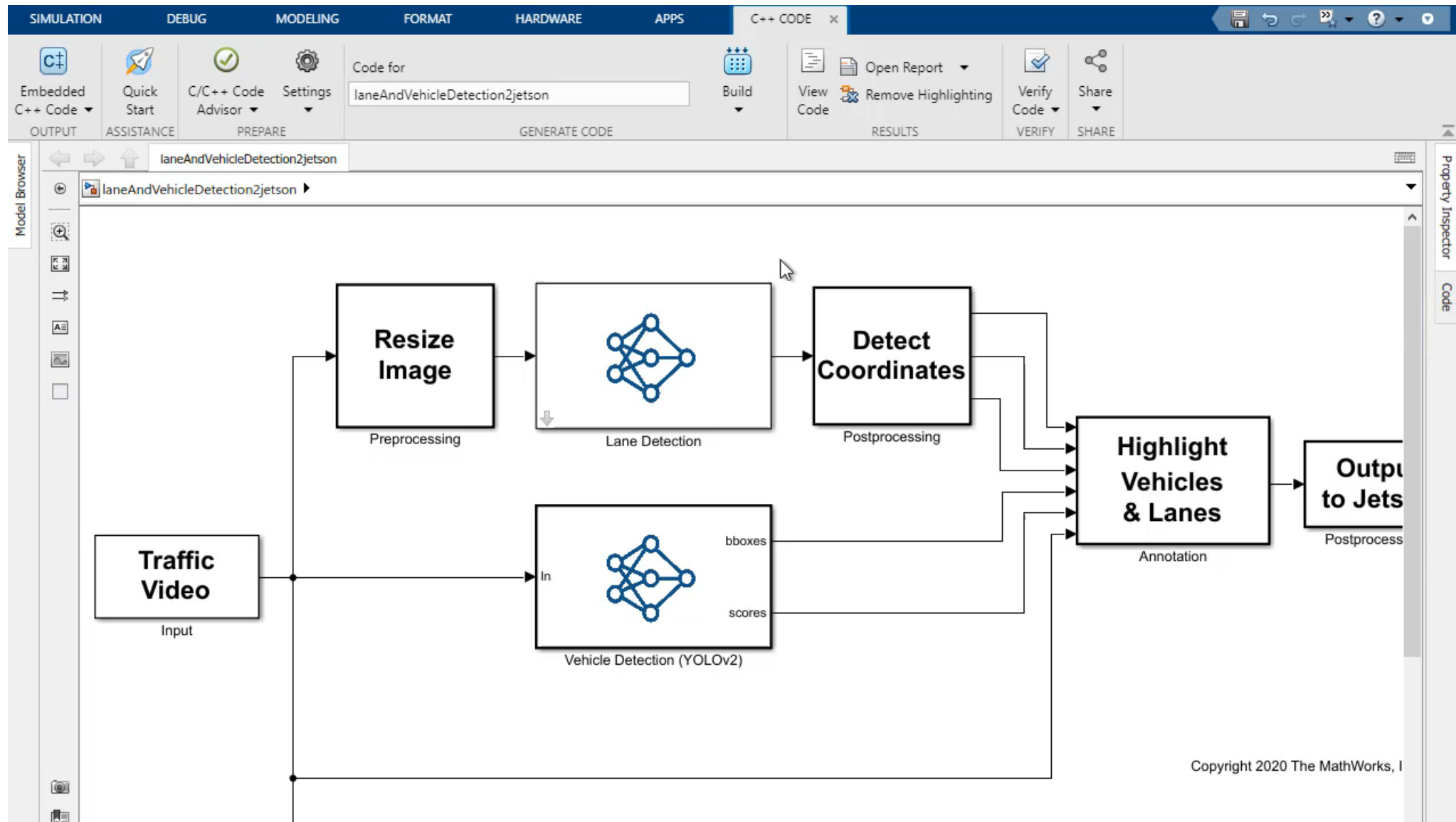




# Run on Jetson AGX Xavier

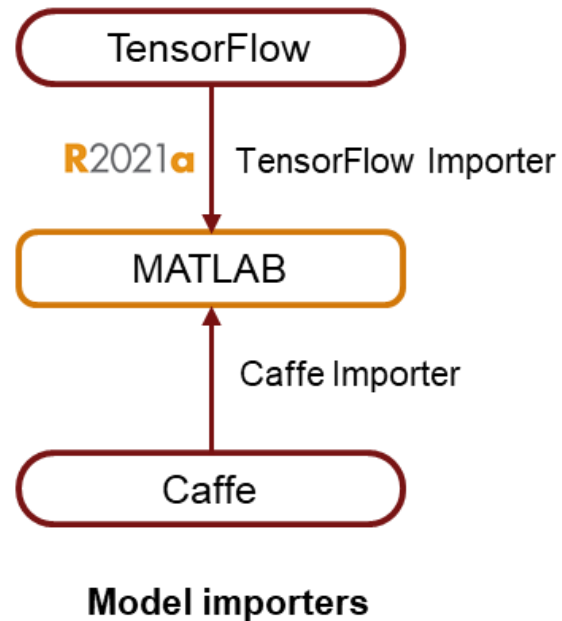
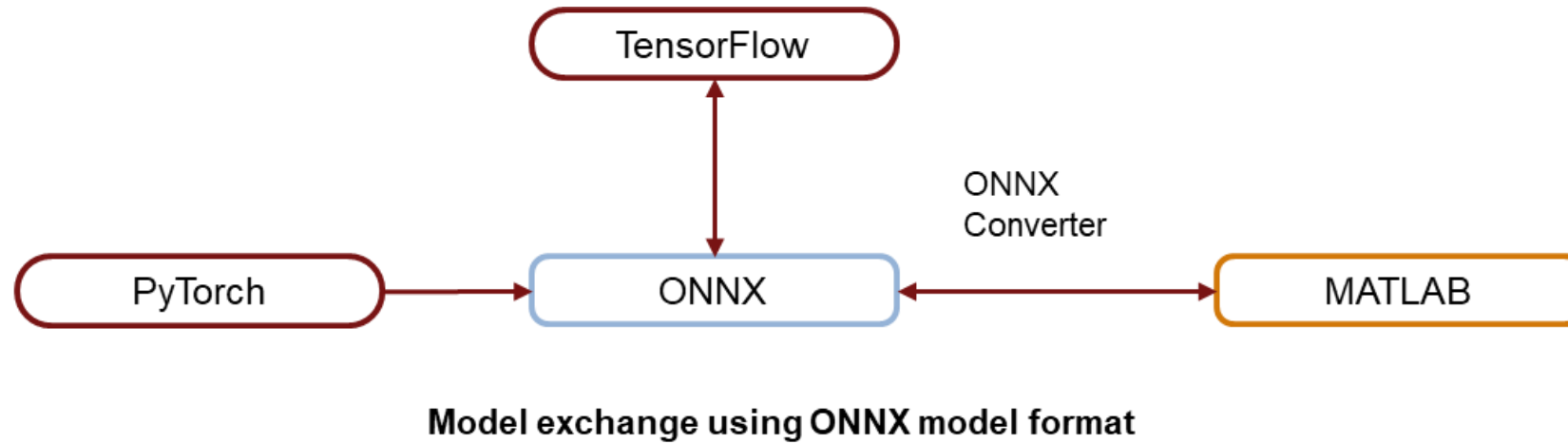


# Run on Jetson AGX Xavier



# Import Trained Models

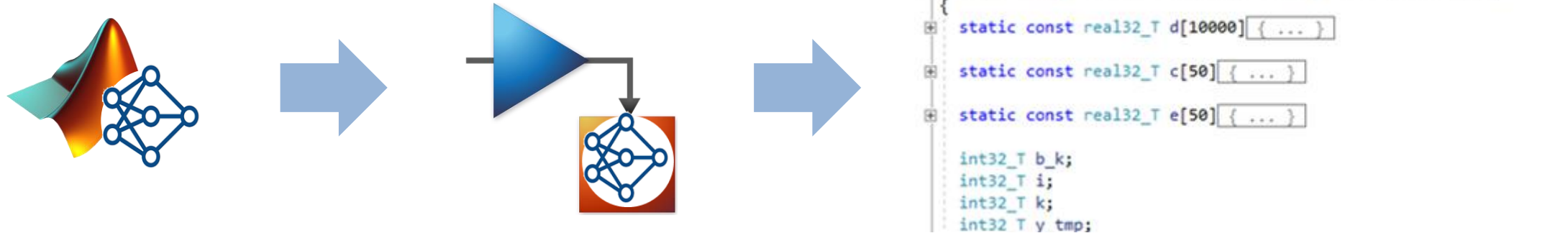
# External Deep Learning Framework Support



# Poll questions

- **Where would you deploy AI?**
  1. **CPU (including microprocessor & ECU)**
  2. **GPU**
  3. **FPGA**

## User Story - Denso Ten



[www.matlabexpo.com](http://www.matlabexpo.com)

*“A model-based development workflow is essential in order to use AI for control ECUs. Combining the existing control model and the AI model enables us to establish a simulation environment and accelerate product development.”*

*- Natsuki Yokoyama, Denso Ten*

## Key Takeaways

### **Integrate trained AI models into Simulink**

- Test design in simulation
- Code generation
- Integrate AI models from others

# MATLAB EXPO 2021

Thank you





# Engagement plan

- Dialogue-style presentation
- Hook with angry boss recording
- Polls on slides 6, 18, 32
- Handout in the end

### (3) Generate CUDA Code

[Subsystem Report](#)

[Code Interface Report](#)

[Traceability Report](#)

[Static Code Metrics Report](#)

[Code Replacements Report](#)

[Coder Assumptions](#)

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**Generated Code**

**[-] Main file**

[ert\\_main.cu](#)

**[-] Model files**

[laneAndVehicleDetection2.cu](#)

[laneAndVehicleDetection2.h](#)

[laneAndVehicleDetection2\\_private.h](#)

[laneAndVehicleDetection2\\_types.h](#)

[trainedLaneNet0\\_laneAndVehicleDetect](#)

[trainedLaneNet0\\_laneAndVehicleDetect](#)

[yolov2ResNet50VehicleExample0\\_laneA](#)

[yolov2ResNet50VehicleExample0\\_laneA](#)

```

11675     gpu_Resize_IntBuffer, n, *gpu_Resize_LineBuffer);
11676     cudaMemcpyToSymbol(gpu_Resize, laneAndVehicleDetection2_B.Resize,
11677                       618348ULL, 0ULL, cudaMemcpyHostToDevice);
11678     cudaMemcpyToSymbol(gpu_Resize_Yindex,
11679                       laneAndVehicleDetection2_ConstP.Resize_Yindex, 4540ULL,
11680                       0ULL, cudaMemcpyHostToDevice);
11681     cudaMemcpyToSymbol(gpu_Resize_Yweights,
11682                       laneAndVehicleDetection2_ConstP.Resize_Yweights,
11683                       4540ULL, 0ULL, cudaMemcpyHostToDevice);
11684     laneAndVehicleD_Outputs_kernel2<<<dim3(1U, 1U, 1U), dim3(256U, 1U, 1U)>>>(*
11685     gpu_Resize_LineBuffer, outIdx, n, 0);
11686     cudaMemcpyFromSymbol(laneAndVehicleDetection2_B.Resize, gpu_Resize,
11687                         618348ULL, 0ULL, cudaMemcpyDeviceToHost);
11688 }
11689 }
11690
11691 // End of Outputs for SubSystem: '<Root>/Preprocessing'
11692 cudaMemcpyToSymbol(gpu_Resize, laneAndVehicleDetection2_B.Resize, 618348ULL,
11693                   0ULL, cudaMemcpyHostToDevice);
11694
11695 // Outputs for Atomic SubSystem: '<Root>/Preprocessing'
11696 // Product: '<S4>/Product'
11697 laneAndVehicleD_Outputs_kernel3<<<dim3(302U, 1U, 1U), dim3(512U, 1U, 1U)>>>
11698 (*gpu_null);
11699
11700 // End of Outputs for SubSystem: '<Root>/Preprocessing'
11701 cudaMemcpy(&laneAndVehicleDetection2_B.dv[0], gpu_null, 1236696ULL,
11702          cudaMemcpyDeviceToHost);

```

# (5) Run on CPUs

ARM CPU  
Generic CPU 