

# Secure, Automated, Internet-based mmWave Testing with Xilinx RFSoC

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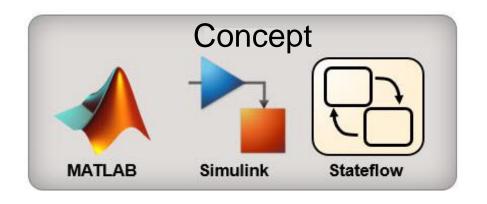








# Technology Design Cycles are Expanding

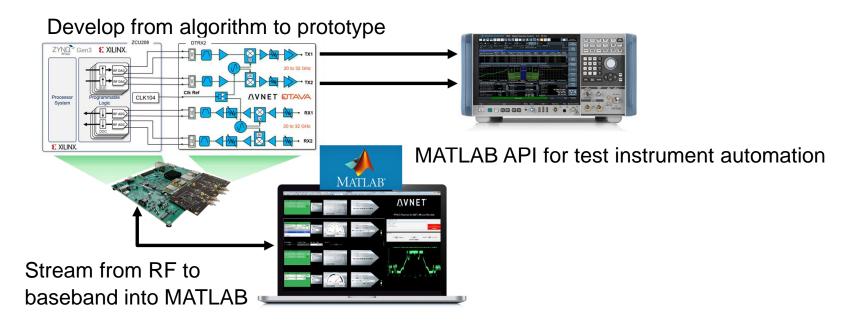




2+ years



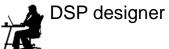
### Xilinx RFSoC Design Cycle





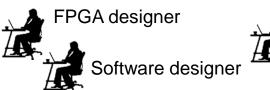
# RF Characterization System-level modeling Simulation Influence

RF engineer



### **Product Design**

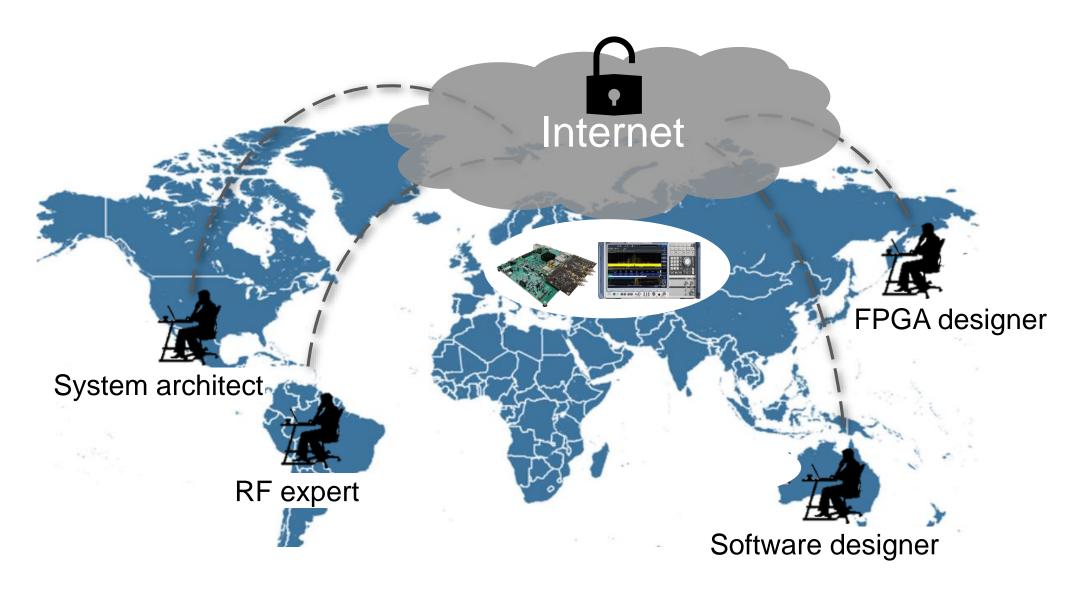
FPGA design
Schematics & layout
Software design start
Test and validation





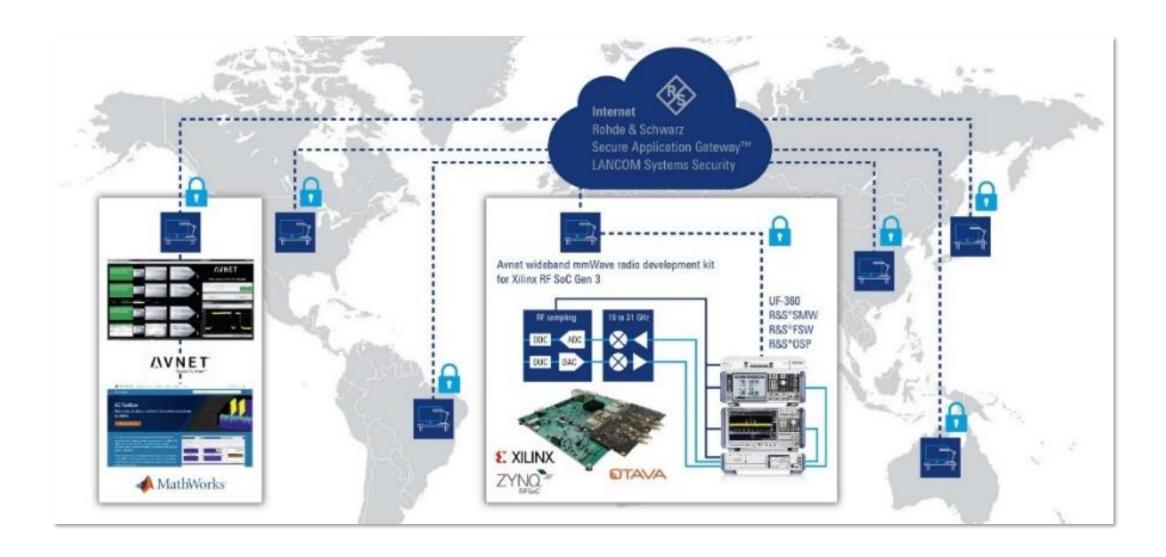


# Distributed Design Teams



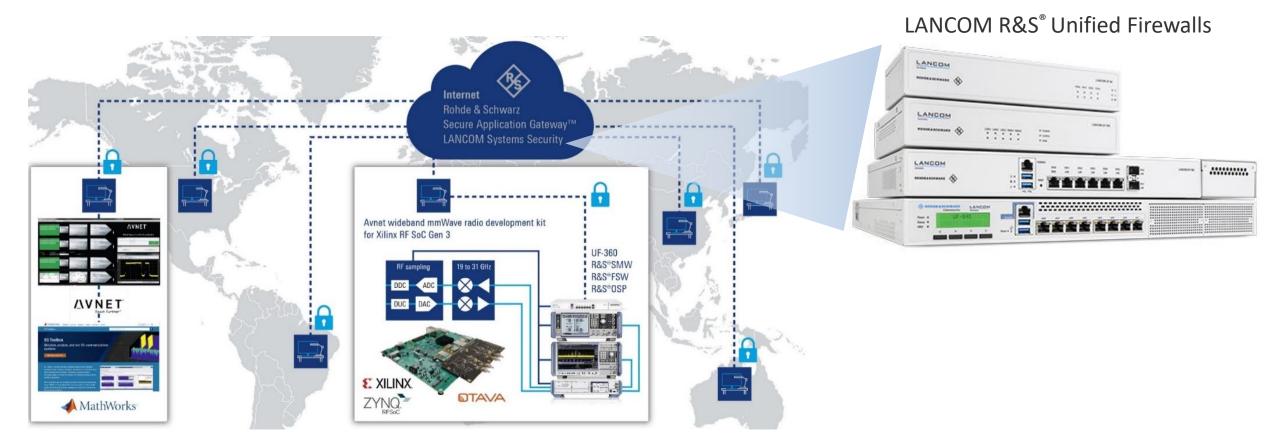


# Secure, Automated, Internet-based mmWave Testing with Xilinx RFSoC





# Rohde & Schwarz Secure Application Gateway Network security by design



- Connectivity to test instruments through MATLAB-based RFSoC Explorer
- State-of-the-art security and Unified Threat Management



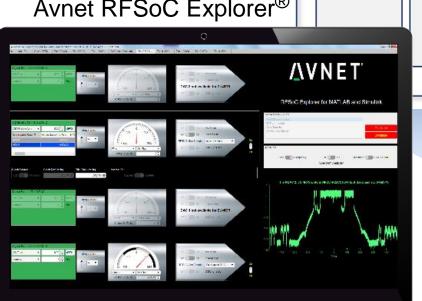
## Avnet mmWave Radio Kit for AMD/Xilinx Zyng® UltraScale+™ RFSoC

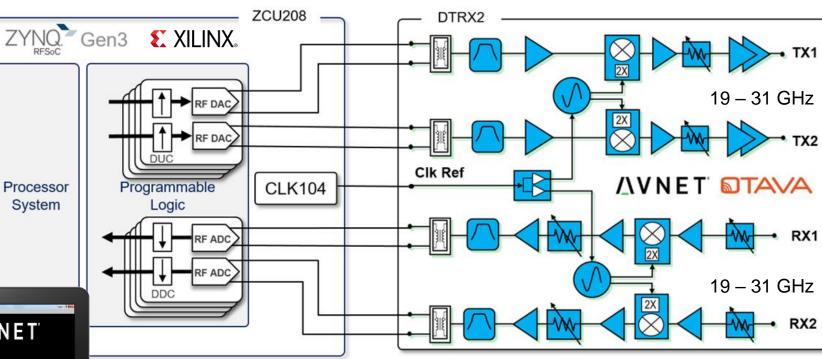
#### Requires:

- **MATLAB**
- Communications Toolbox
- DSP System Toolbox
- **Fixed-Point Designer**
- Signal Processing Toolbox



Avnet RFSoC Explorer®



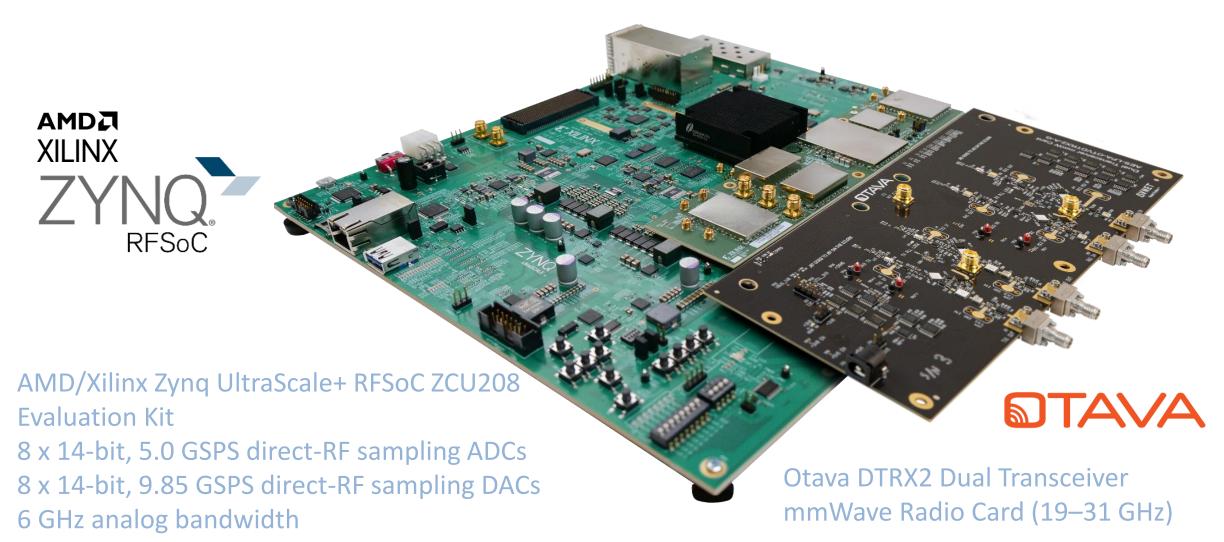






#### Radio under Test

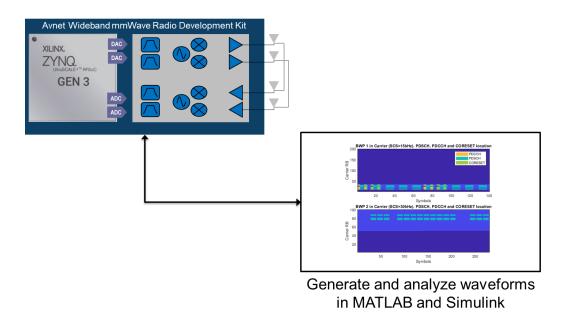
# Avnet Wideband mmWave Radio Development Kit for Xilinx RFSoC Gen-3



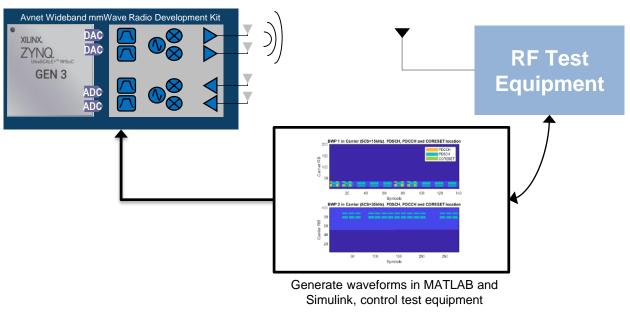


#### **Traditional Test & Measurement**

# Hardware Evaluation in loopback testing



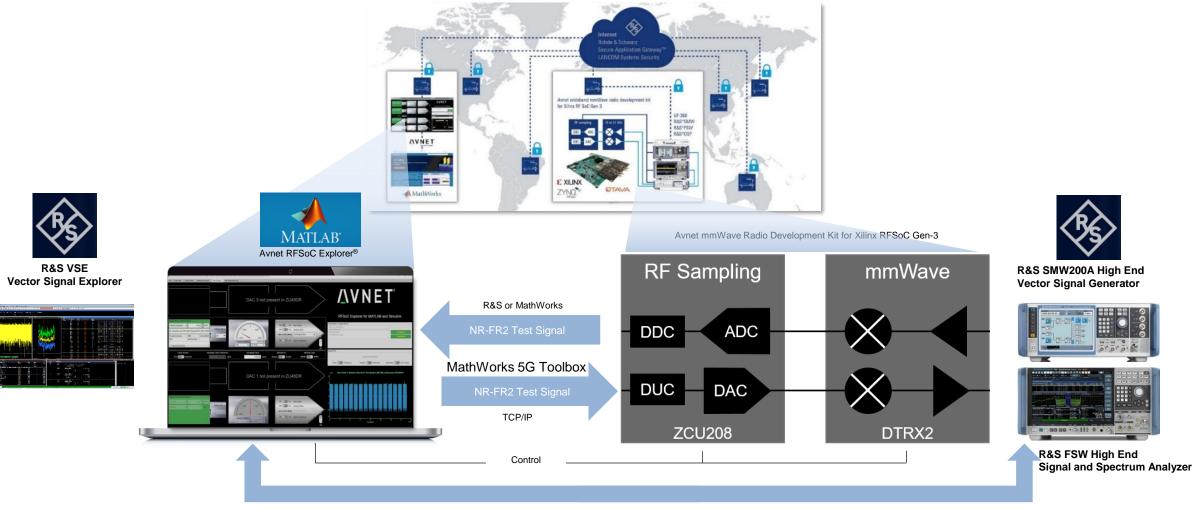
# Over-the-air Testing



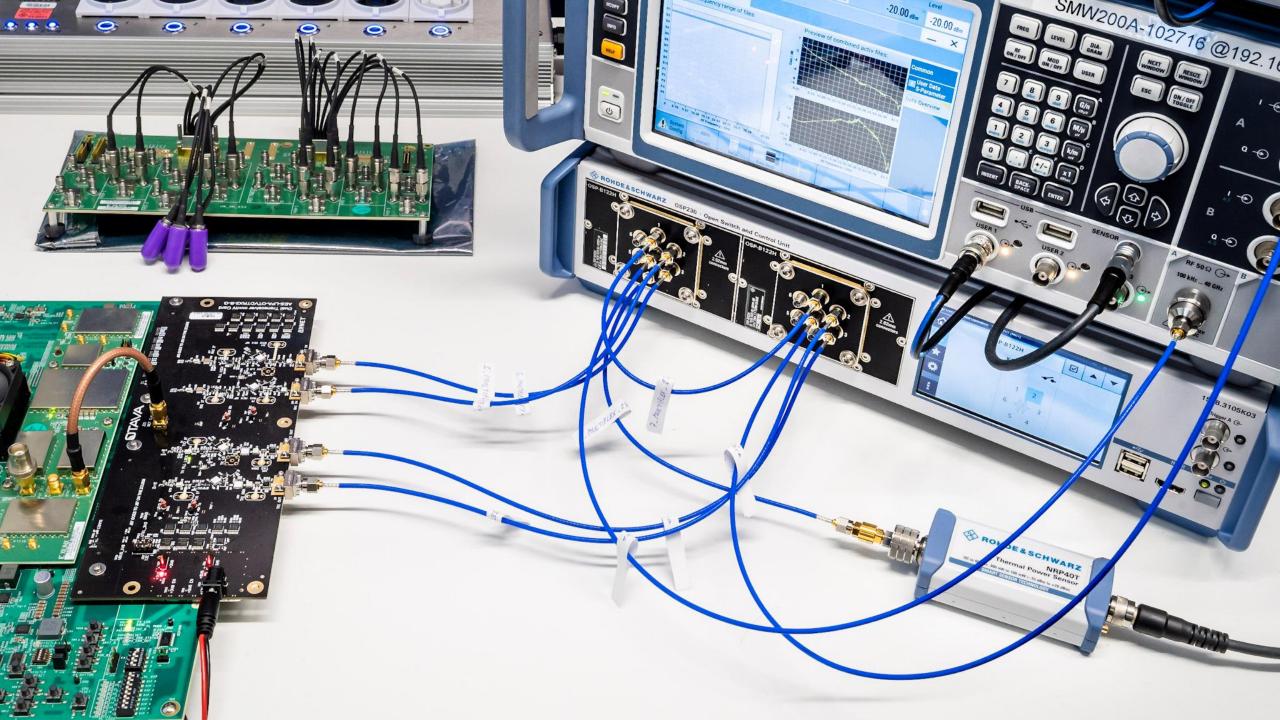
Lacking closed-loop instrument control / MATLAB integration



# Our Reference Design Testing

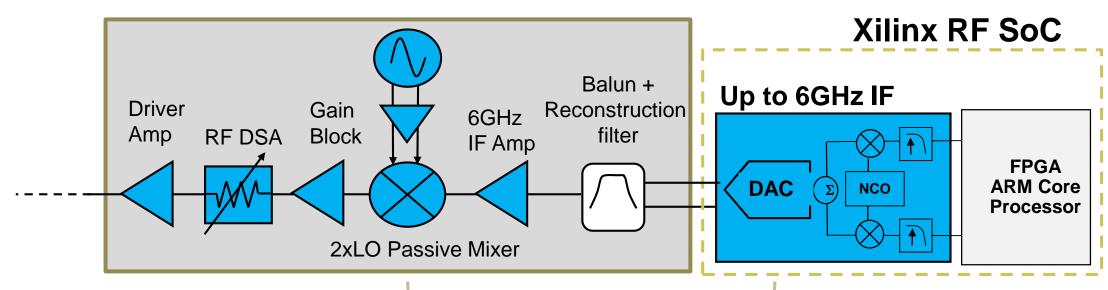




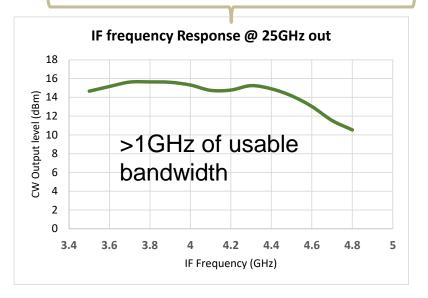




## The RF-sampling Tx Signal Chain



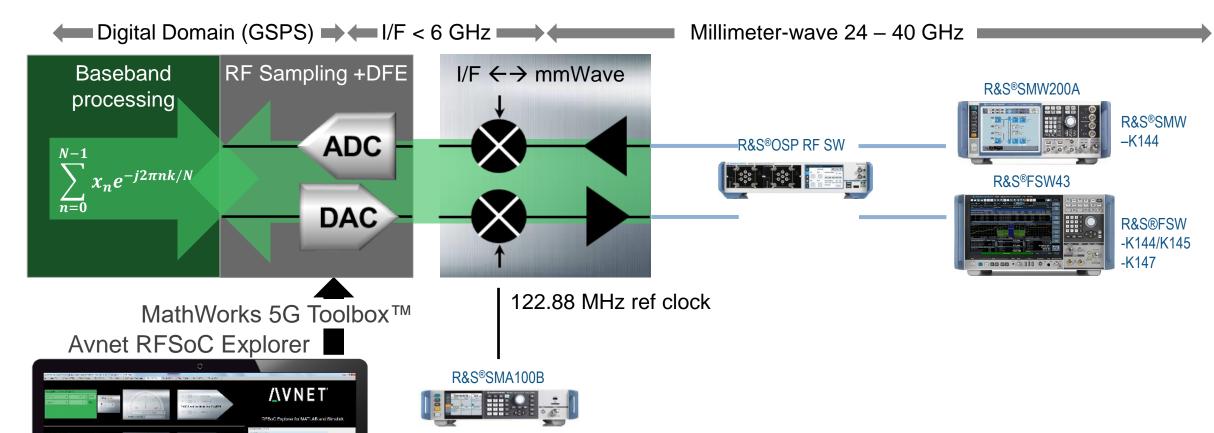
How to achieve best wideband RF performance?



Focus on optimizing wideband 5GNR performance for the whole signal chain



## Demo: Automated Test Sequencing with User MATLAB Functions

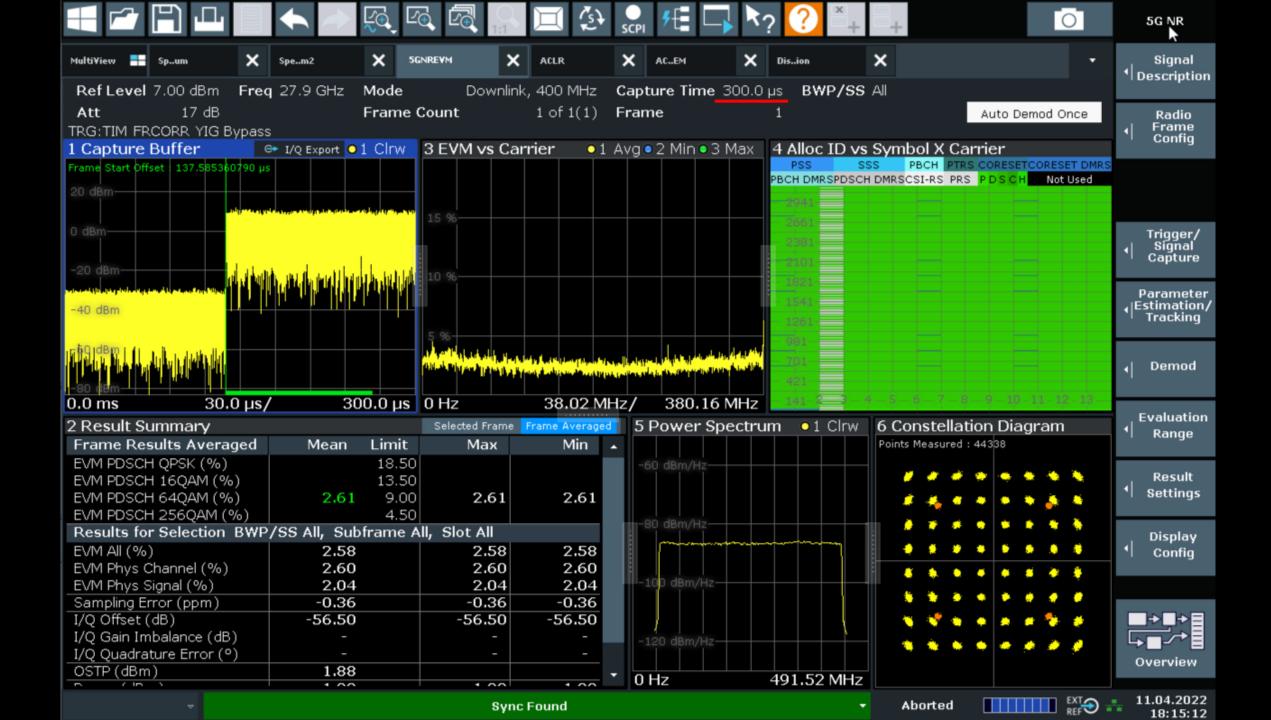


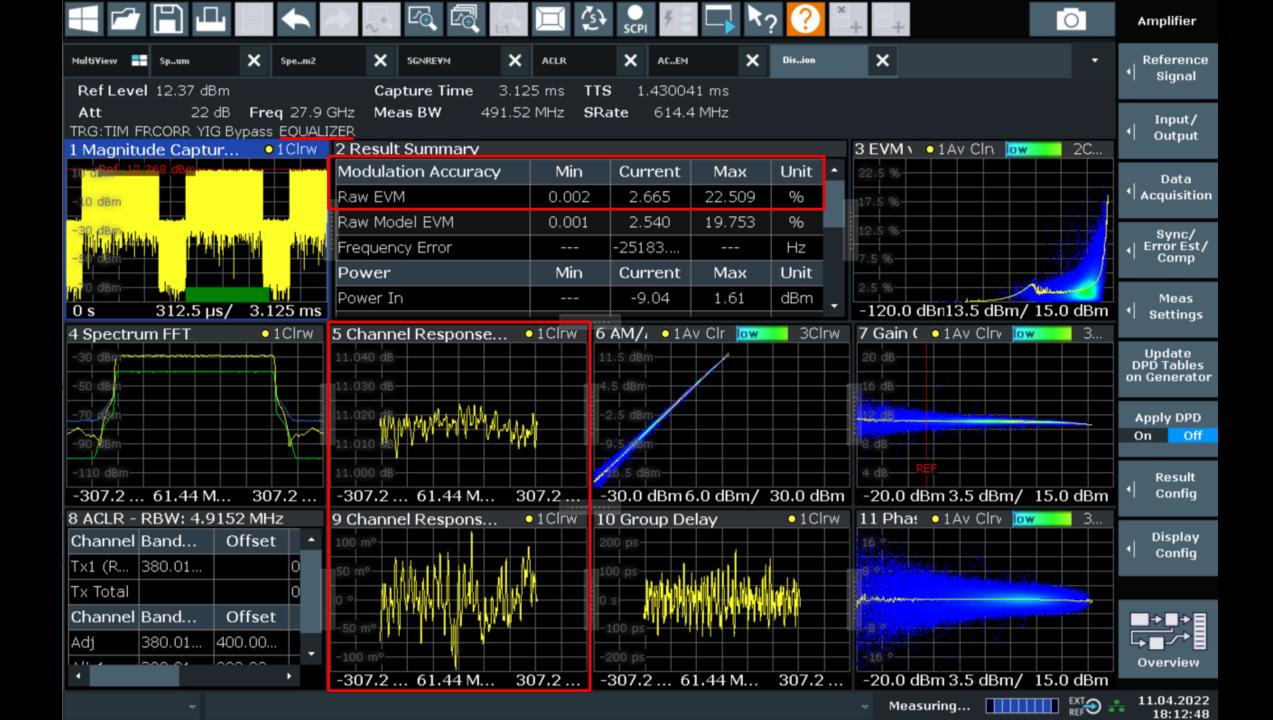
Automated wideband 5GNR FR2 TDD testing:

- 1. Combined EVM / ACLR / SEM measurements,
- 2. Channel Response in Magnitude and Phase with and without equalization

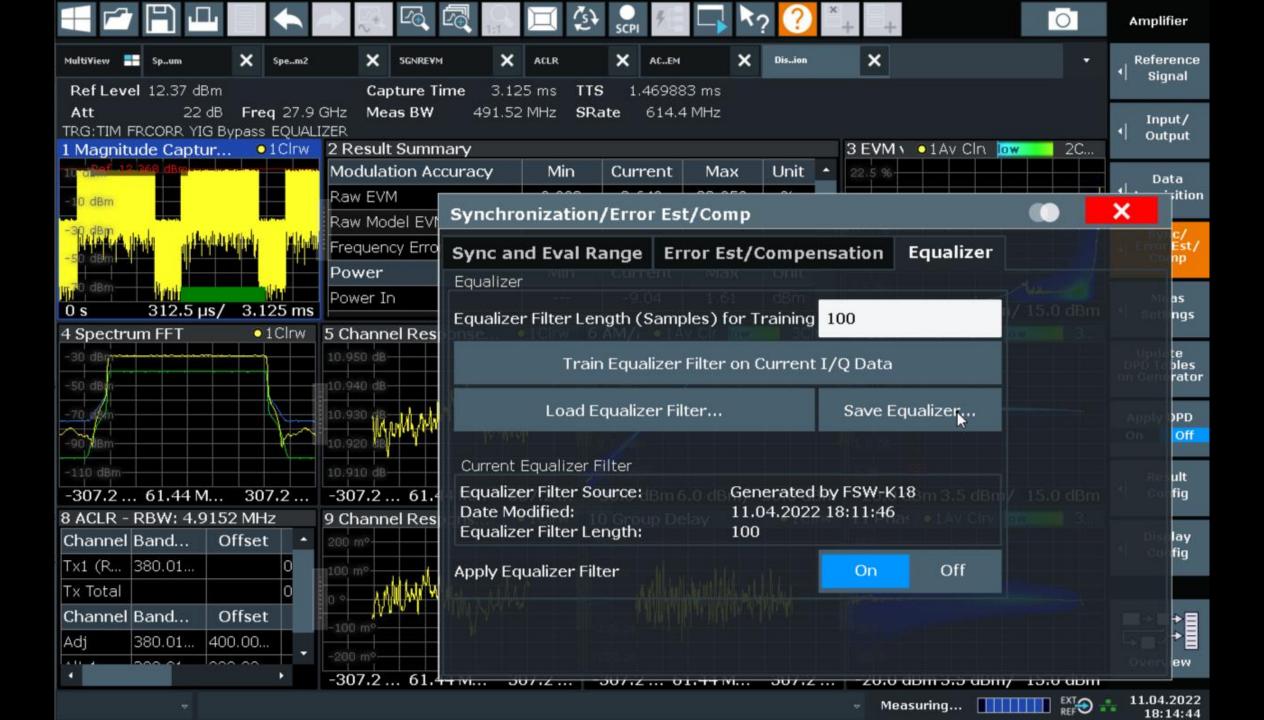


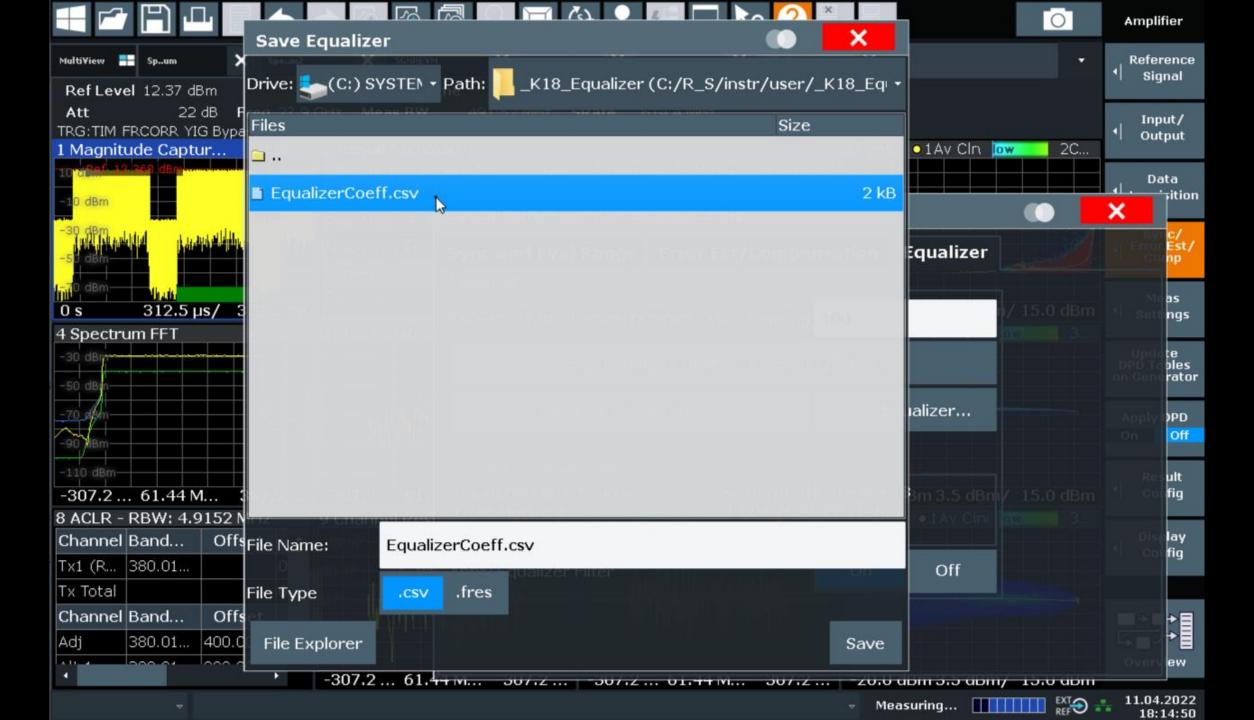






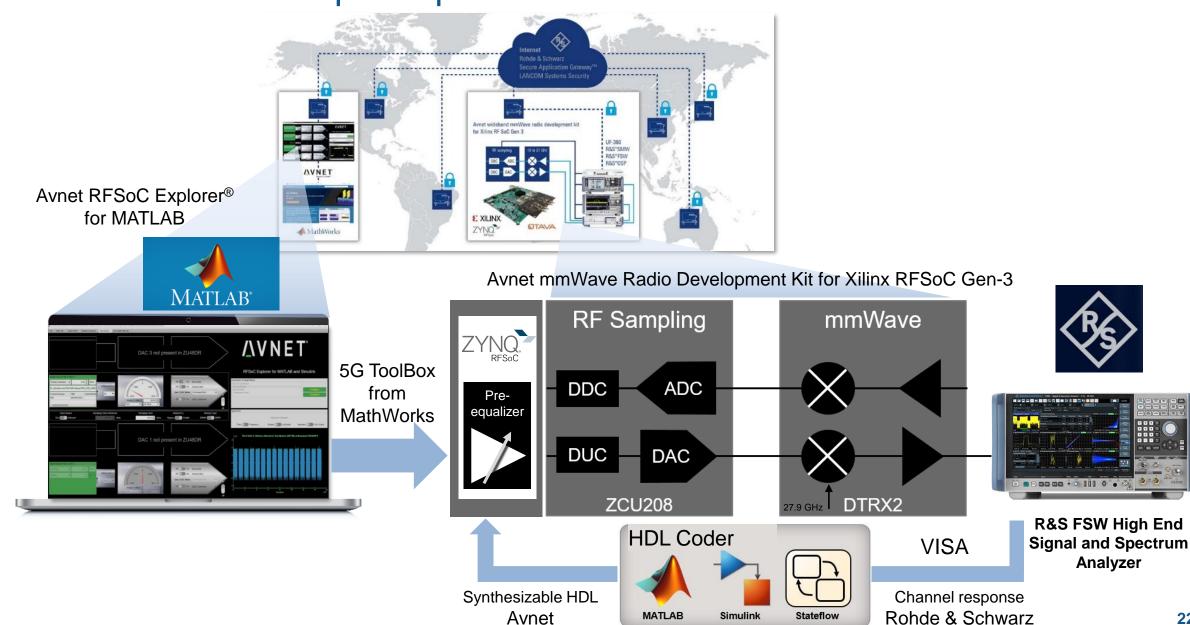






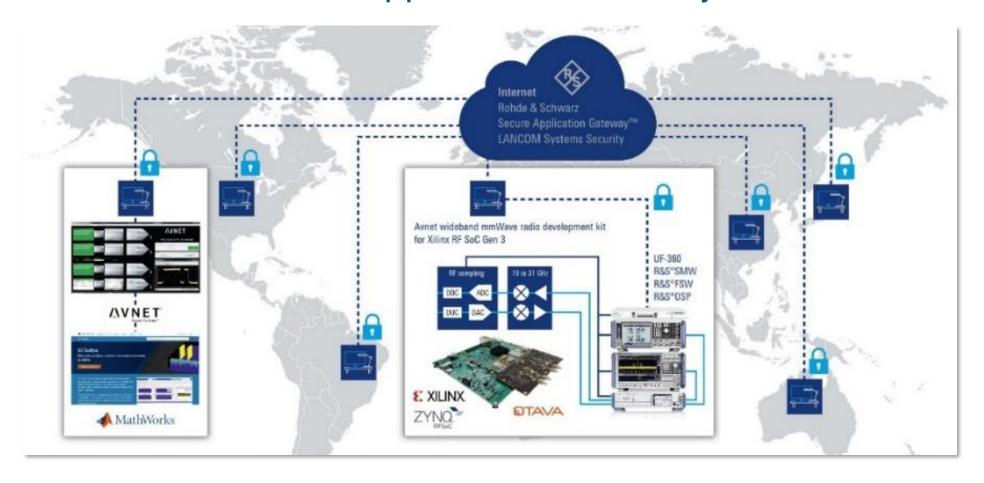


### Instrument-assisted pre-equalizer in Xilinx RFSoC Gen-3





### Rohde & Schwarz Secure Applications Gateway



- Join engineers from Avnet, Otava and Rohde & Schwarz for guided tour of automated remote testing
- Experience 'virtual hands-on' access as though the test equipment were on your desk
- Register for the Avnet workshop at: <a href="https://www.avnet.me/mmw-workshop">www.avnet.me/mmw-workshop</a>



#### Conclusion

- mmWave RF systems require secure, robust test automation
- Avnet RFSOC Explorer enables AMD/Xilinx RFSoC within MATLAB with Rohde & Schwarz MATLAB support for 'instrument in-the-loop' standard compliance testing
- Rohde & Schwarz Secure Application Gateway™ platform enables worldwide collaborative remote access in real-time with state-of-the-art data security

# MATLAB EXPO

# Thank you



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