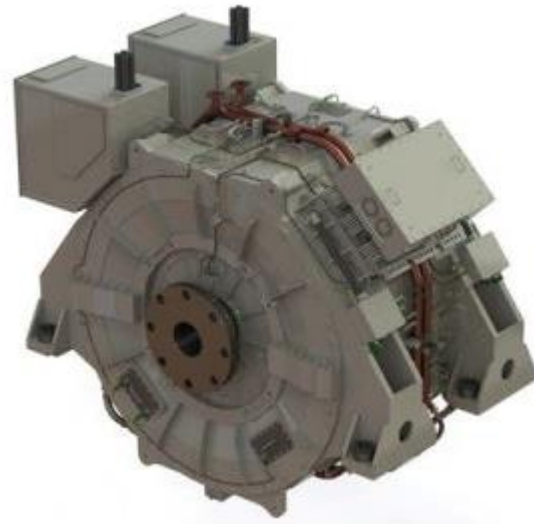


Who is Leonardo DRS?

We build large scale marine power electronics systems



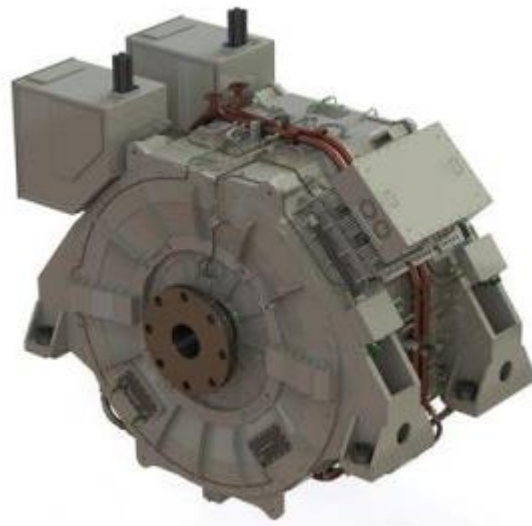
Large Propulsion Motor



Large Propulsion Motor Drive

Electric Drive Hardware-in-the-Loop (HIL) Testing: Skip the Beta Phase!

Henry Brengel : Senior Design Engineer

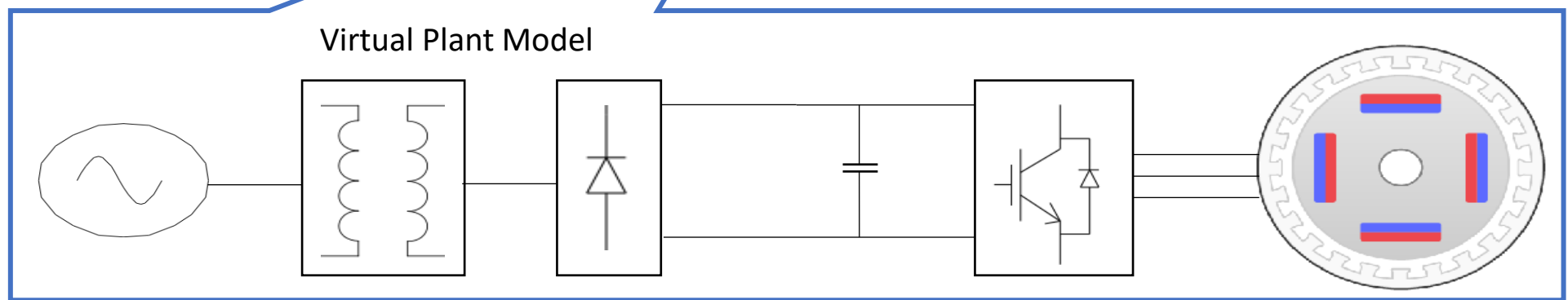
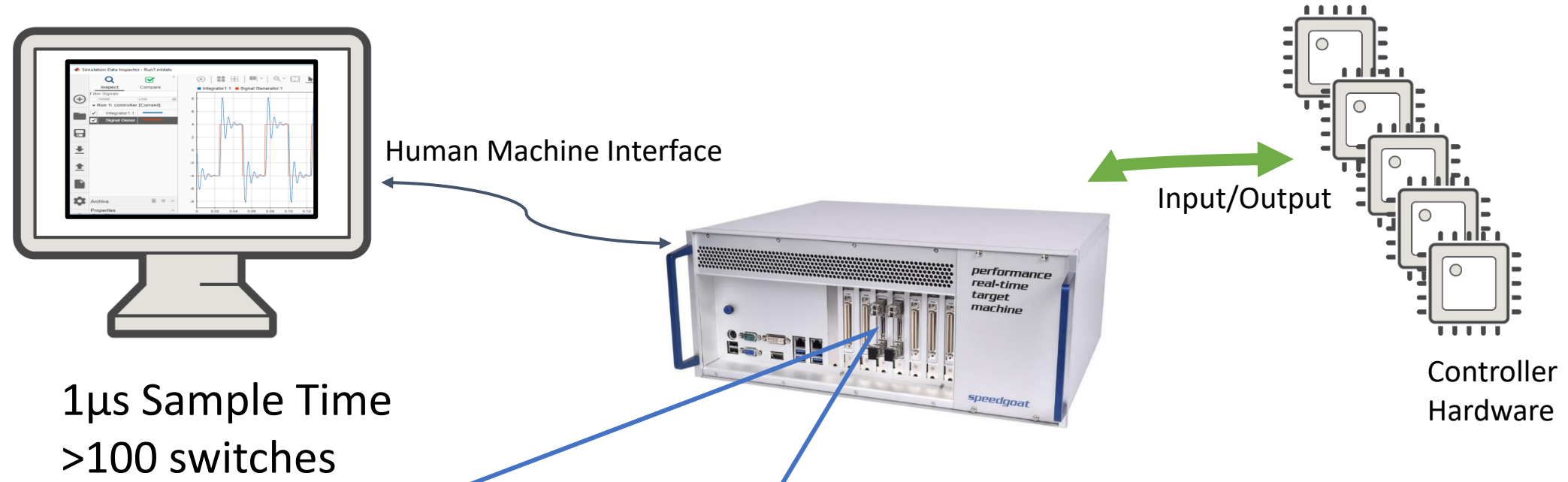



Large Propulsion Motor



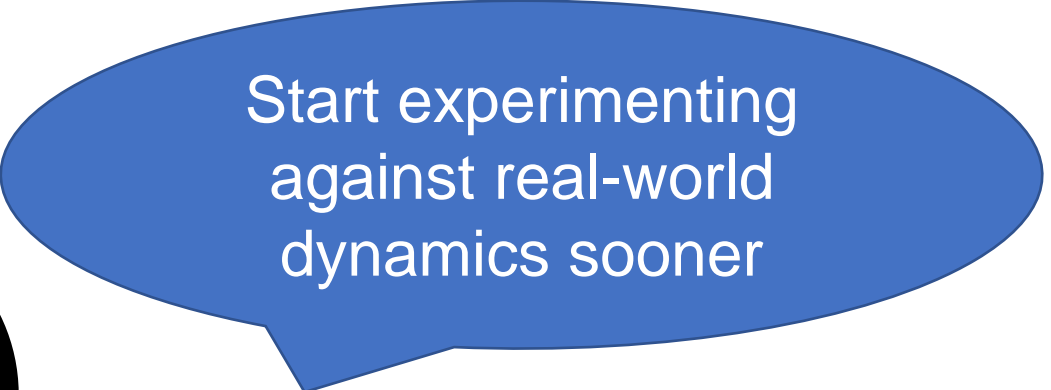
Large Propulsion Motor Drive

Hardware-in-the-loop (HIL) Testing- Power Systems Drive





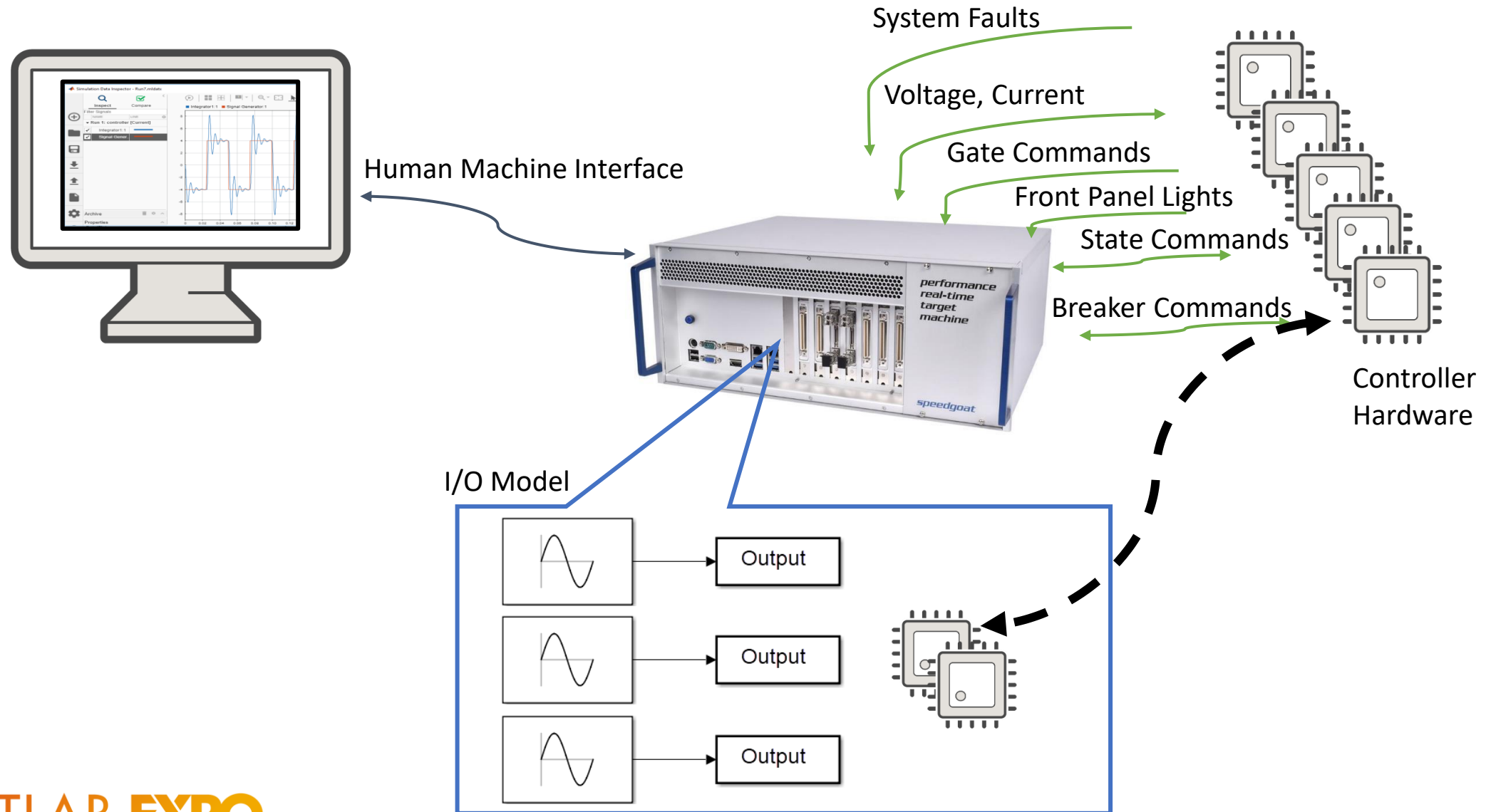
Equipment is big and
expensive
Needs to work well
And work well the first
time



Start experimenting
against real-world
dynamics sooner



Hardware-in-the-loop (HIL) Testing- Distributed Communication

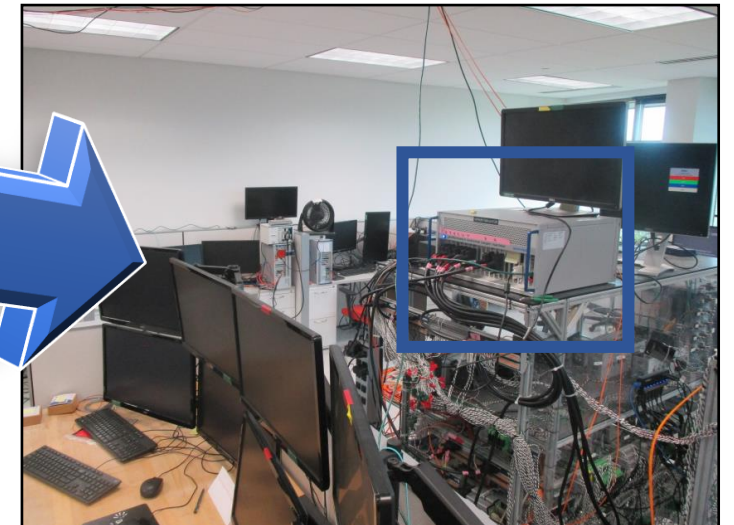
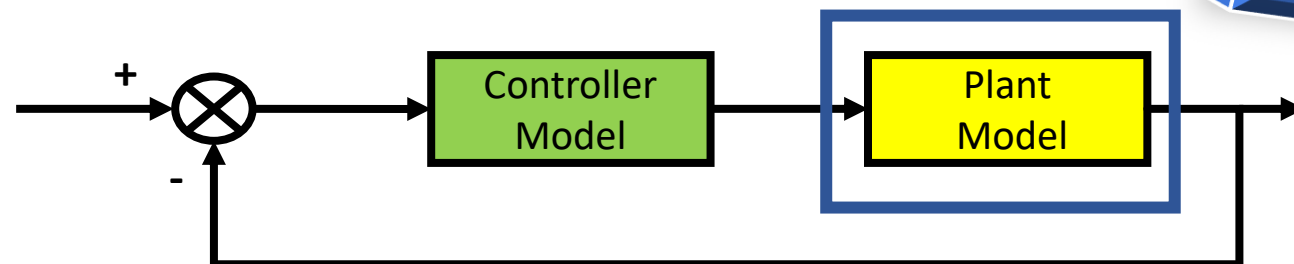


Benefits

- Closed loop real-time motor control validation
- Digital Twin
- Design process improvement- Enabled Intergroup and Intragroup communication
- Simulation models reused for HIL testing

Overview

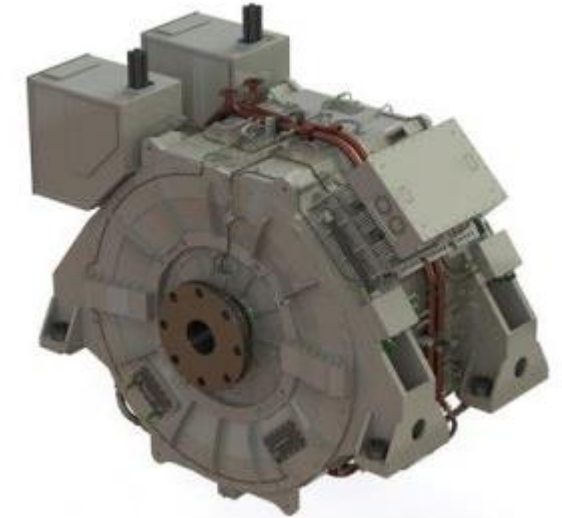
- Problem statement overview
- Approach: Model-Based Design and HIL
- Why HIL?
- Why FPGAs are useful to power electronics HIL?
- Additional Benefits



Problem: Process

How does DRS fully test an integrated electric power converter before delivery to the customer when:

- Purchasing equipment for complete high power testing costs millions of dollars
- Procurement of additional test infrastructure could negatively impact scheduling
- Test time on hardware is costly, resource intensive, and limited in coverage



Large Propulsion Motor



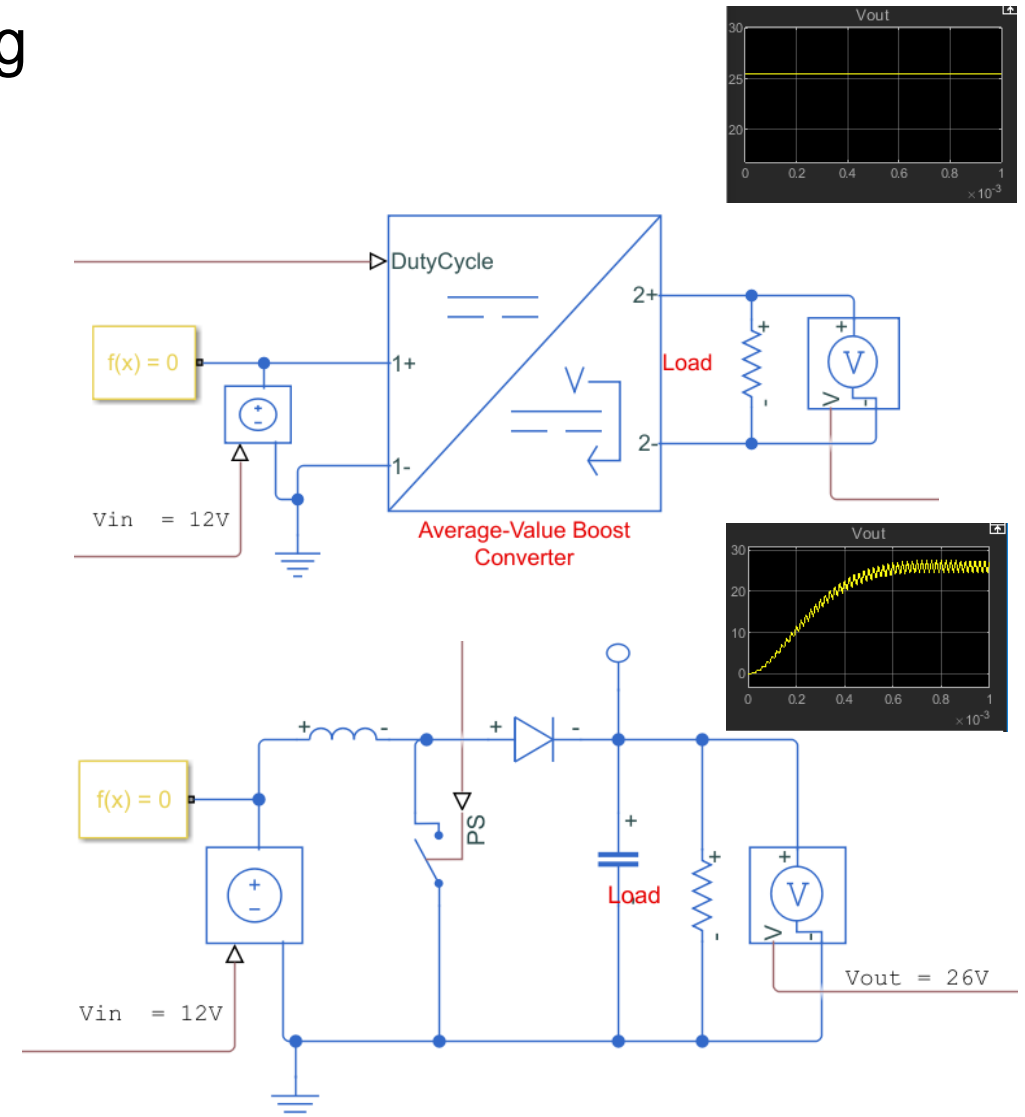
Large Propulsion Motor Drive

HIL Architecture: Average Value Mode vs Switched-Linear Mode

Boost Converter: 2 Approaches to Simulating Power Electronics

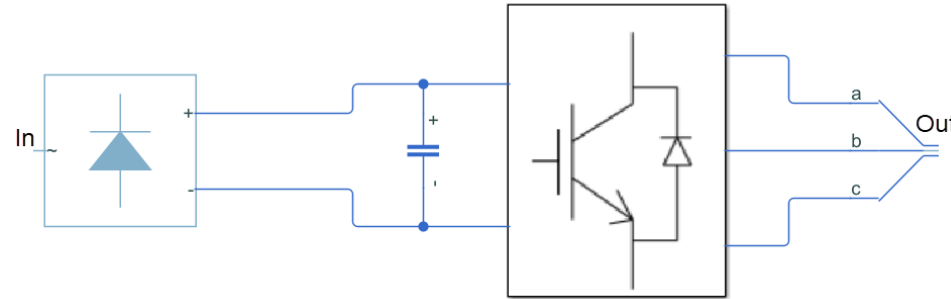
- Average Model
 - Works for larger discrete step sizes
 - Ignores dynamics of switching devices
 - Supports many types of analysis

- Switched Model
 - Requires small discrete step sizes
 - Captures switching dynamics
 - Better estimates losses, timing, etc.

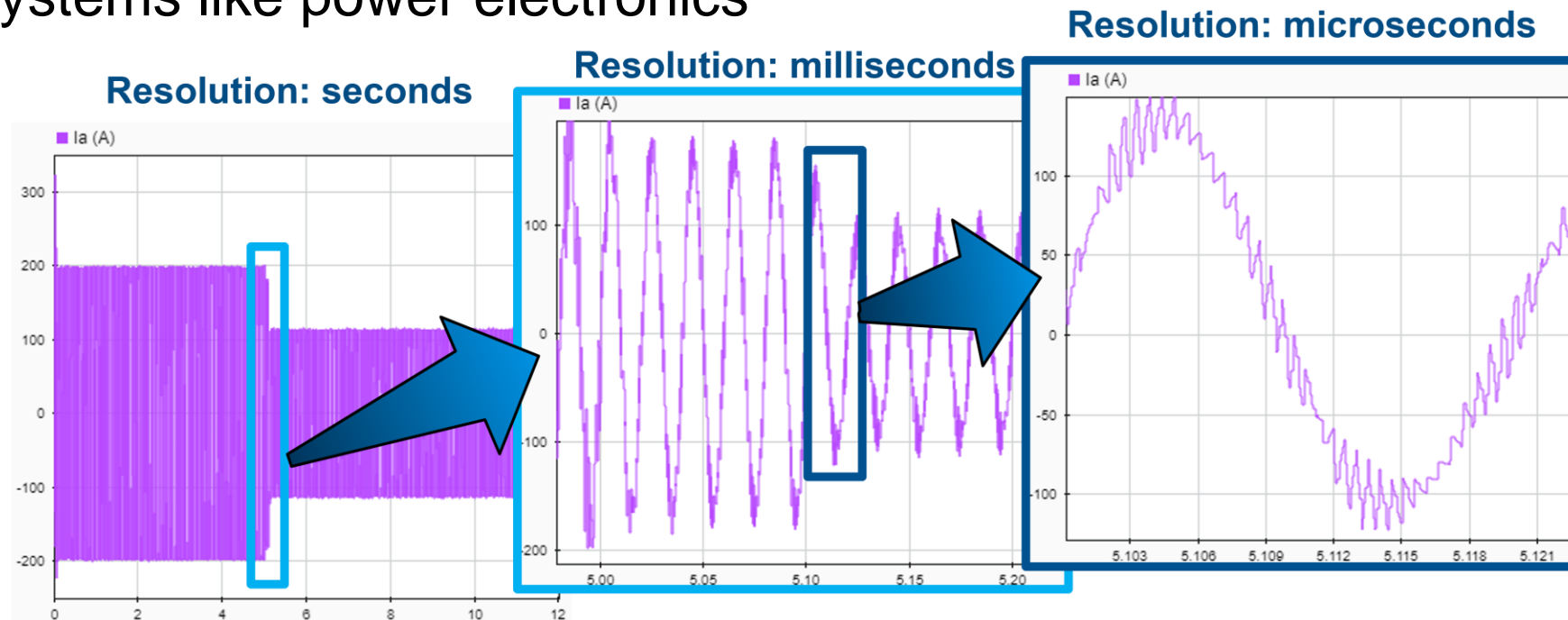


Real Time Simulation: Power Electronics Device Switching

Time step of
micro/nanoseconds

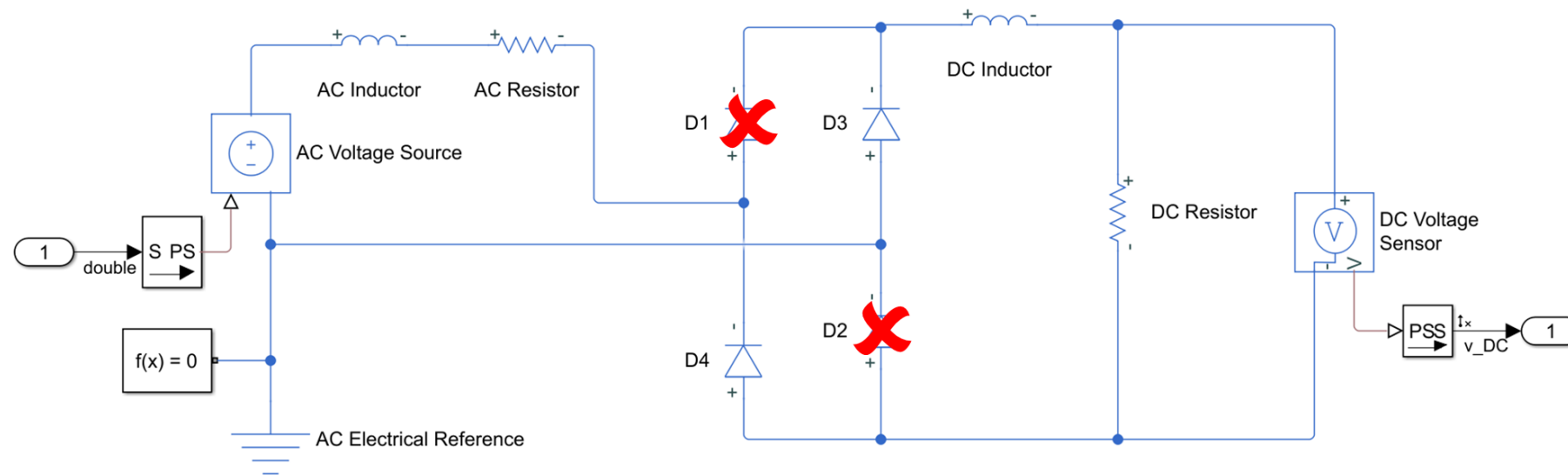


- High sample rates (small time steps) are required to capture fast transients in systems like power electronics



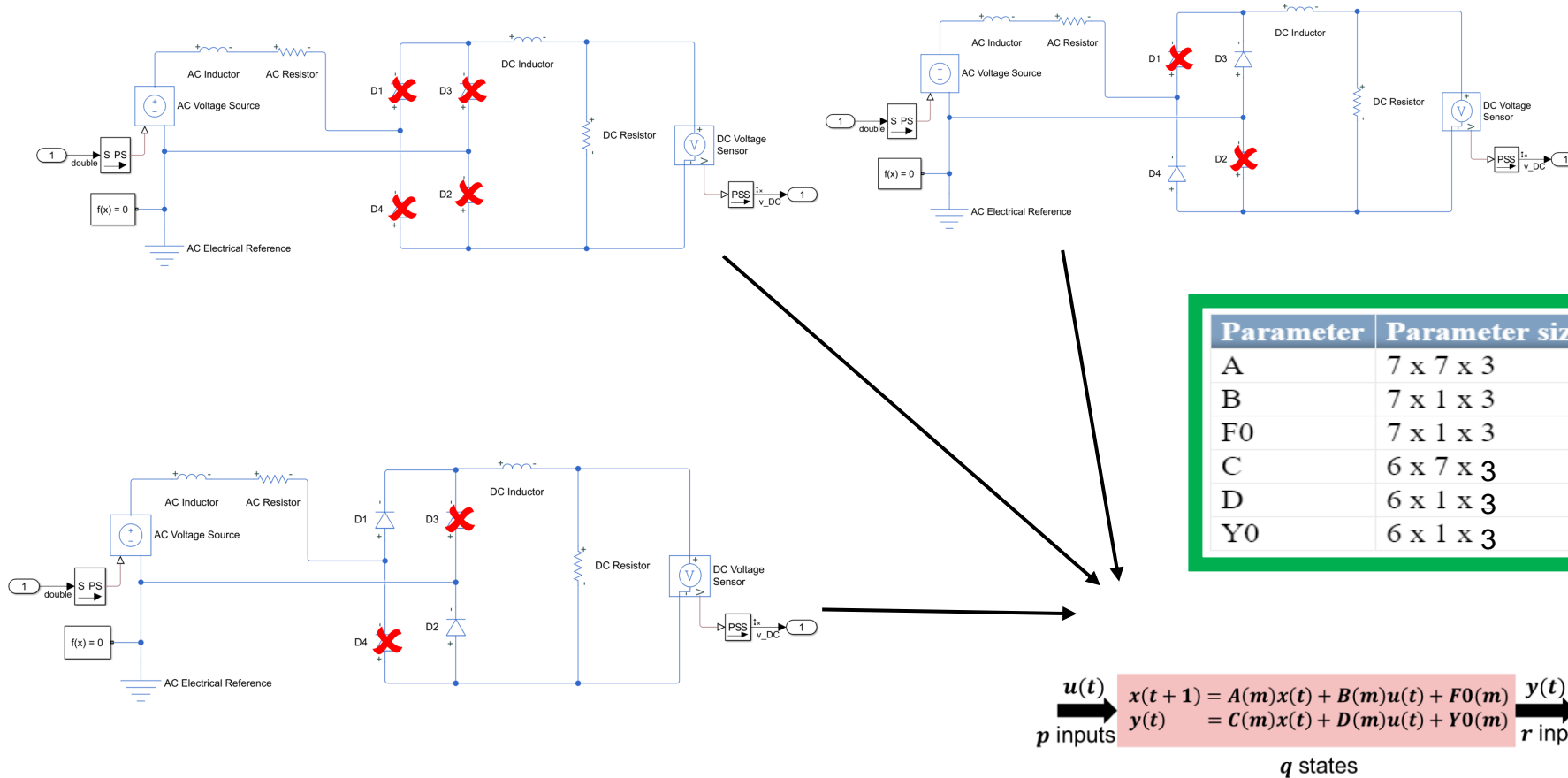
Implementation: State-space Switched Linear System

Bridge rectifier contains 3 configurations



Implementation: State-space Switched Linear System

Bridge rectifier contains 3 configurations

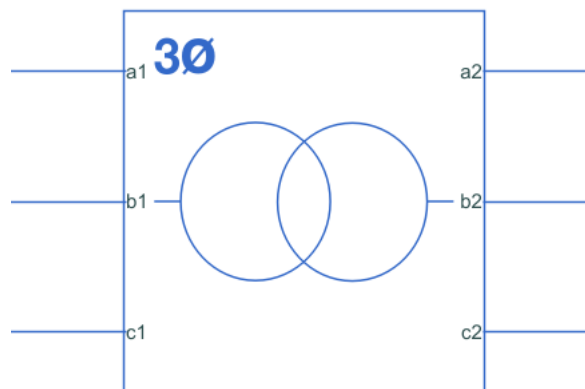


Each configuration is represented by a separate set of state-space matrices

Large Power Systems HIL Architecture

- FPGA resources and timing – n switches; 2^n configurations
- Split plant network on slower dynamic coupling locations to lower 2^n configurations

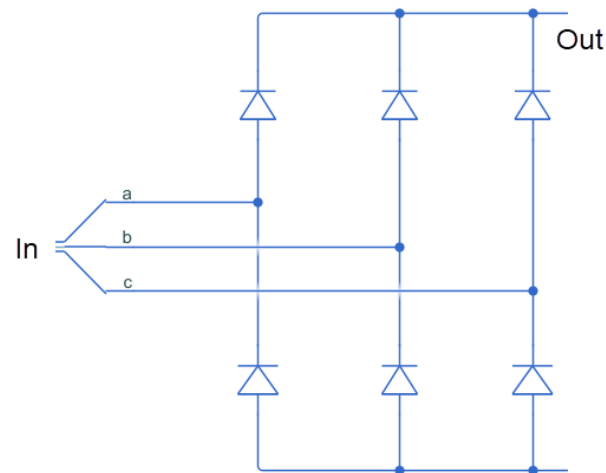
Transformer
3-Phase



0

Switches

Rectifier

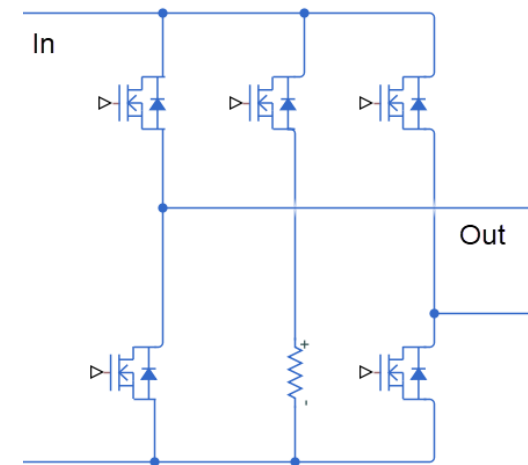


6

Switches

Total 11
Switches

H-Bridge

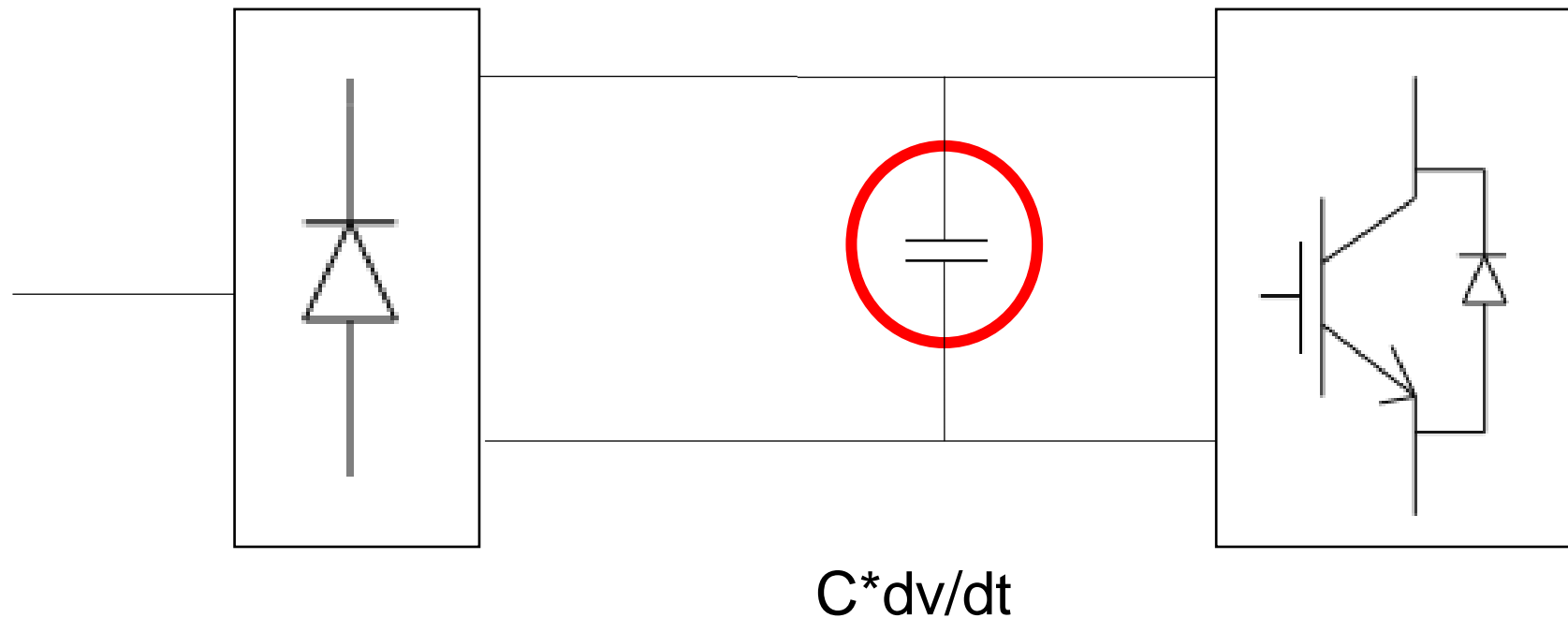


5

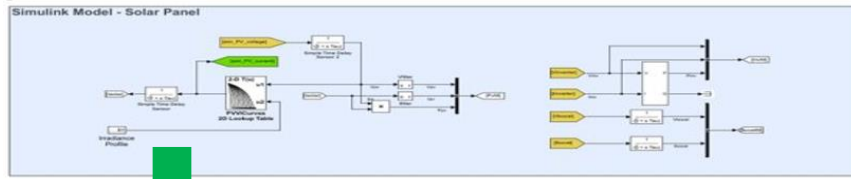
Switches

HIL Architecture : Splitting Networks

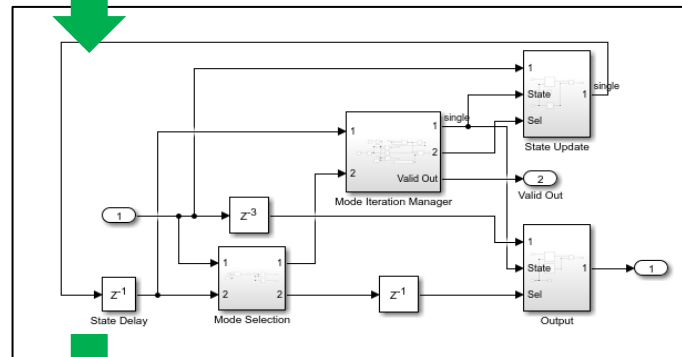
- Break up the plant system on capacitive or inductive energy boundaries to lower 2^n switches per network – each network now has its own solver



Implementation : HIL Testing using Simulink Real-Time



Simscape HDL Workflow Advisor



HDL Workflow Advisor

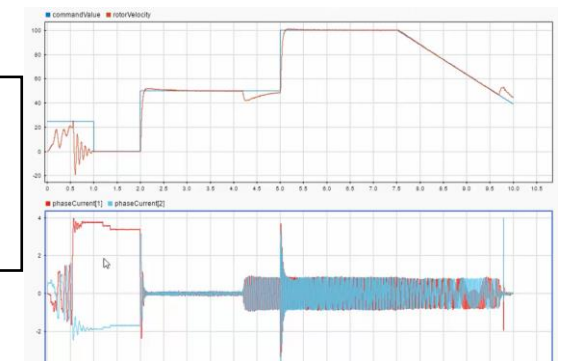


Simulink Model

Workflow to deploy physical models to Speedgoat

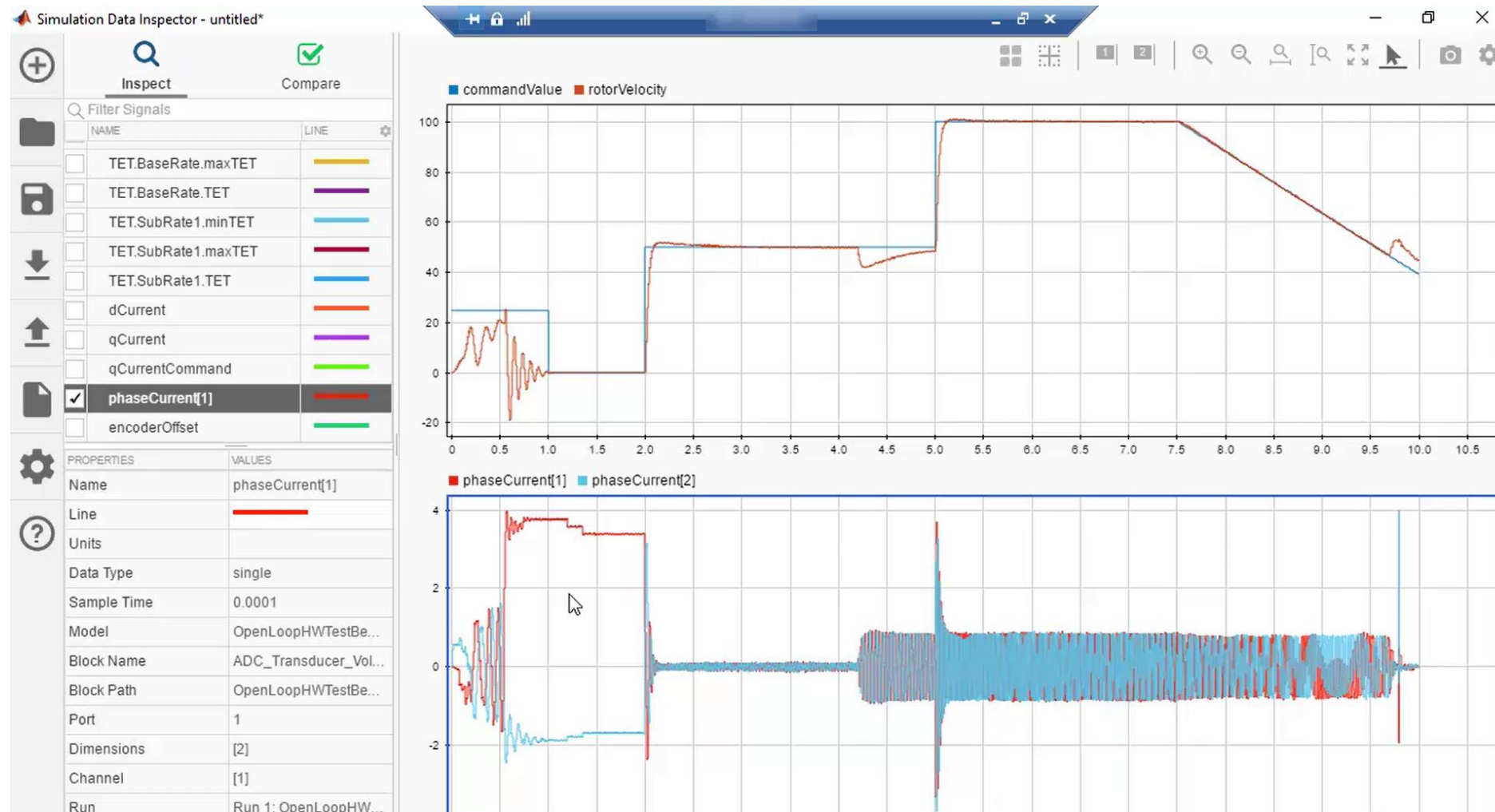
HDL Compatible Simulink model

Simulink Real-Time HIL Testing Automation

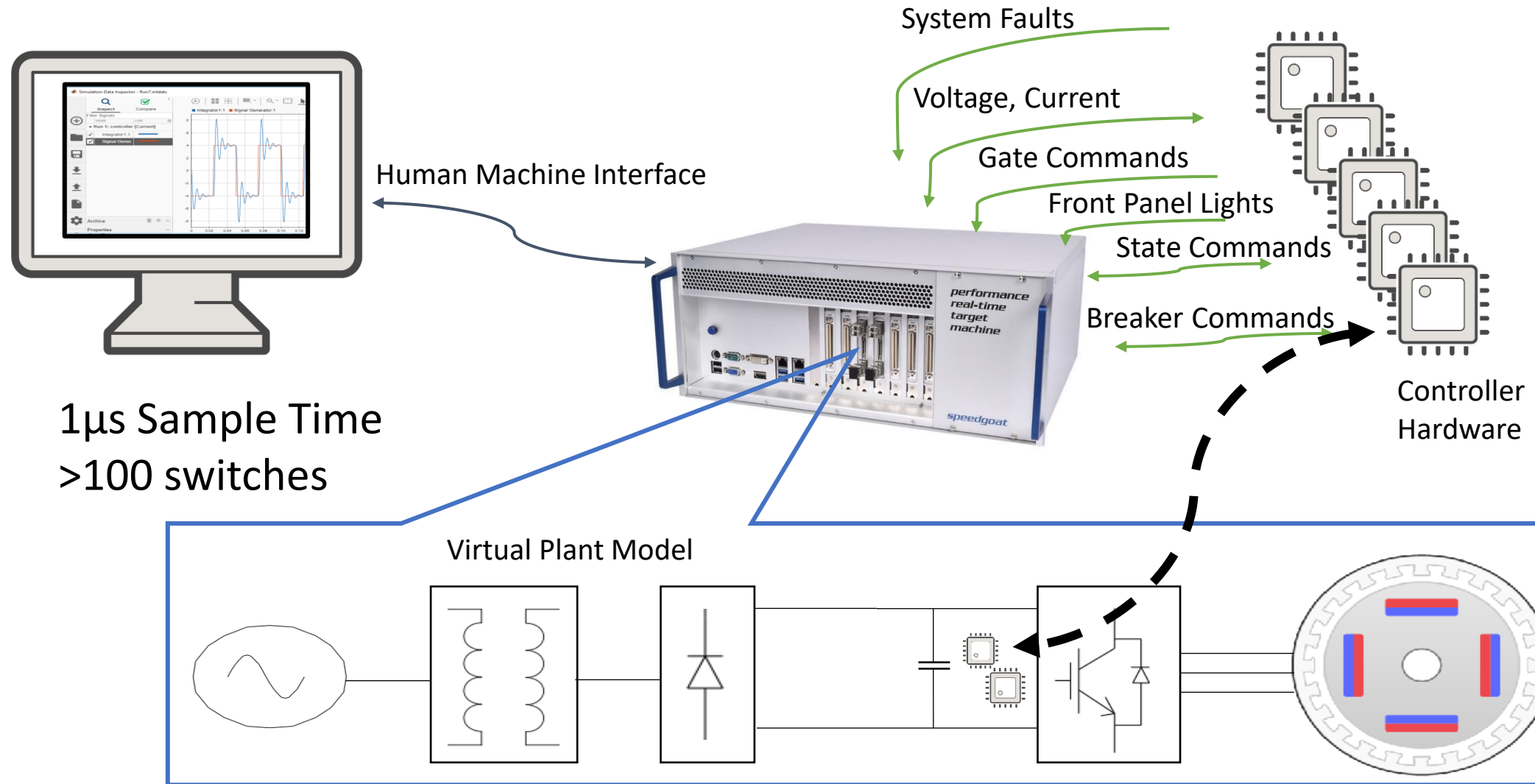


Speedgoat Target –Virtual Plant Running on FPGA

Simulation Data Inspector

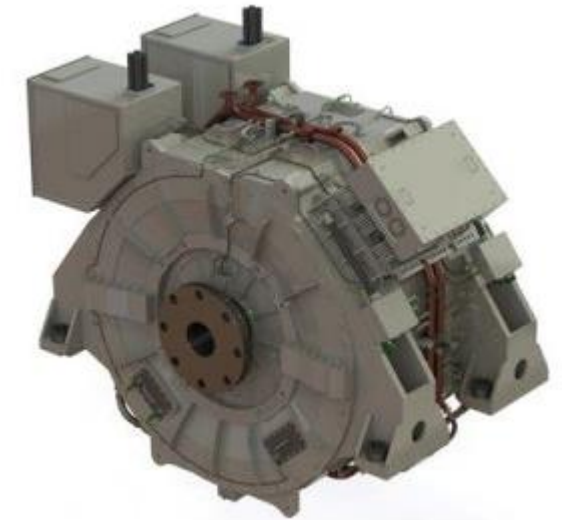
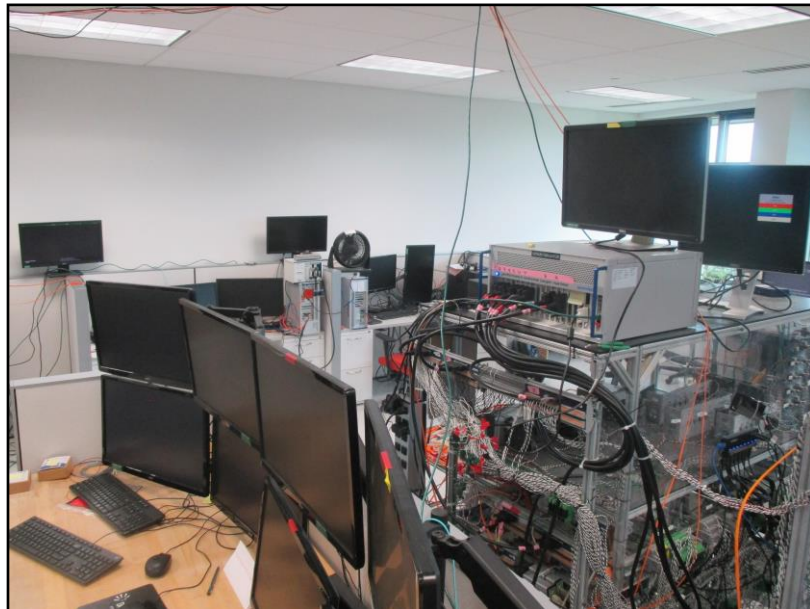


Hardware-in-the-loop (HIL) Testing- Power Systems Drive



Additional Benefits

- Design process improvement- Enabled Intergroup and Intragroup communication
- Digital Twin



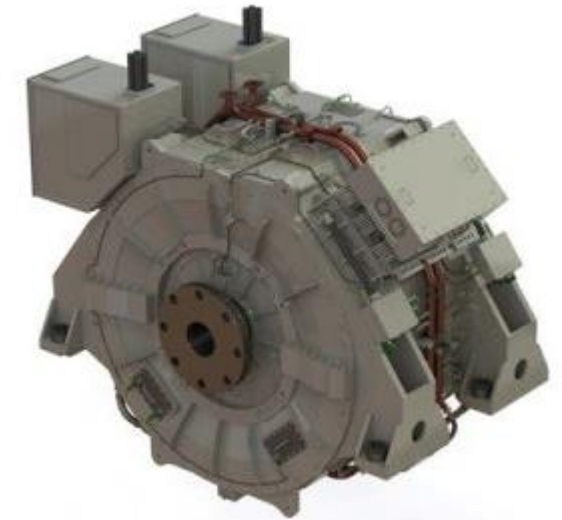
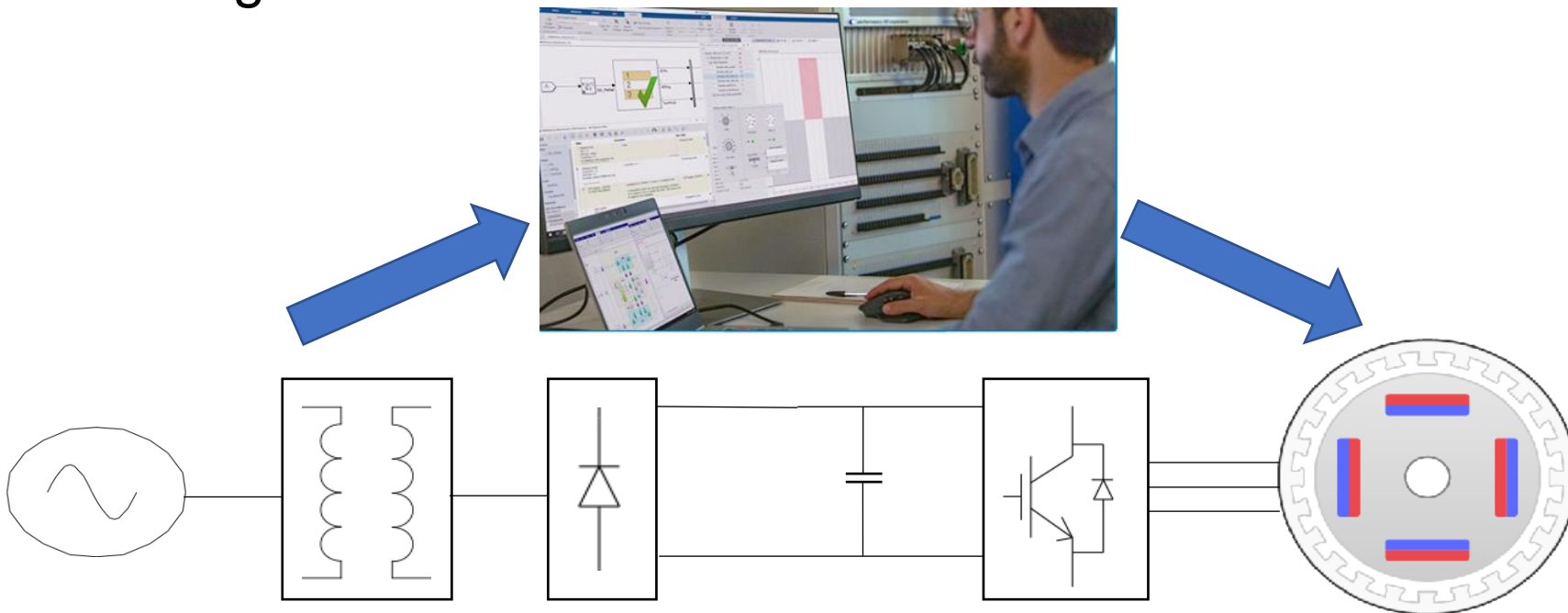
Large Propulsion Motor



Large Propulsion Motor Drive ¹⁸

Additional Benefits

- Design process improvement- Enabled Intergroup and Intragroup communication
- Digital Twin



Large Propulsion Motor



Large Propulsion Motor Drive 19

Summary

- Simulation models reused for HIL testing.
- Controller (propulsion drive under test) accurately tested against a physics-based plant model.
- Cost, time, and lab space saved.
- Design iterations reduced from days to hours.

Questions?