

Developing Onboard SOH Estimation Using DVA and ICA for LFP Batteries

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- Differential Voltage & Incremental Capacity Analysis
- Cyclic & Calendaric Aging
- Model Development
- Development Workflow
- Summary

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Degradation Mechanism

Most dominant

Anode

Lithium Plating, Solid-Electrolyte-Interphase (SEI)

Cathode

Structural Stress, Transition Metal Dissolution

Electrolyte

Electrolyte Decomposition

Collector

Collector Corrosion

Separator

Clogged Pores



Li-Ion Batteries suffer from a variety of degradation mechanism

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Degradation Mechanism



Mechanism can be organized in three degradation modes:

- Loss of Lithium Inventory LLI
- Loss of active anode material LAM_A
- Loss of active cathode material LAM_c





Incremental Capacity Analysis





The ICA converts the voltage plateaus of a two-phase transition into detectable peaks. Graphite undergoes several phase transition during charging/discharging process.

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Voltage of Full Cell / V Cathodepotential vs Li/Li⁺/ V (a) Anodepotential vs Li/Lt / V 0.4 0.3 3.5 0.2 0.1 0 80 10 20 30 40 50 60 70 90 100 Calculated Full Cell dV/dQ of Cathode/Full Cell / V Ah¹ Cathode (b) Anode dV/dQ of Anode / V Ah 300 300 A 200 -200 100 100 0 20 0 10 30 40 50 60 70 80 90 100 SoC of Full Cell / %

Differential Voltage Analysis

$$\frac{dV}{dQ} = \left(\frac{dQ}{dV}\right)^{-1} \approx \frac{V(t_2) - V(t_1)}{Q(t_2) - Q(t_1)}$$

The distance between two peaks of the DV curve represents the amount of charge involved in a two-phase transition.

$$\frac{dV}{dQ} = \frac{d(\varphi_{cathode} - \varphi_{anode})}{dQ} = \frac{d\varphi_{cathode}}{dQ} - \frac{d\varphi_{anode}}{dQ}$$

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Peak and Valleys have simple correlation with capacity fade

Due to LLI, LAM_C & LAM_A, caused by cyclic ageing, the position and height of observable peaks and valleys shift in various directions.

Cyclic & Calendaric Aging



ICA & DVA

Cyclic &

Calendaric Aging

Model

Development

Calendaric aged*

Degradation

Mechanism

Cyclic aged*

*NMC EOL = End of Life BOL = Begin of Life

Development Workflow Summary

120

4.2

DVA & ICA Calculation & Filter

SOH LUT Generation

3.6 Discharge Cycle Charge Cycle 3.6 0.1 3.5 3.4 -0.1 2 3.3 -0.2 Cell Voltage -0.3 -0.4 3 @ const. T, I_{charge}, I_{Discharge} -0.5 2.9 2.8 00:00:00 00:30:00 01:00:00 01:30:00 Time

Charge Cycle

- Battery cycled at constant temperature, charging and discharging current
- Conduct every n cycles Check-up cycle



Degradation

Mechanism

ICA & DVA

Cyclic &

Calendaric Aging

Feature

Extraction

- Detect Features in Check-up cycles
- Add information of cell temperature, SOC and voltage of detected feature



• Calculate linear regression model



Development

Workflow

Summary

Model

Development

SOH LUT Generation



 Repeat previous steps for different cells at various temperatures and C-rates Calculate Feature-map by linear interpolation of all linear regression models at various temperatures

 Convert Feature-Map into 2D Look-up Table

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Simulink Model



Development Workflow



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Summary

ICA & DVA

Data Analysis with MATLAB

- e.g. plots, peak detection
- User friendly
- Various helper functions
- Simple visualization

Onboard Implementation

- MBD
- Unit Test with Simulink Test
- C-Code Generation
- V-model coverage through MathWorks products, e.g. Simulink Requirements, Simulink Test, etc.
- ISO26262 & IEC61508 coverage through IEC Certification Kit
- Flexible integration through
 Code Generation

Next Steps

Onboard Implementation

- Data-Generation
- HIL-Testing
- Vehicle Integration

Research & Development

 Enhance Algorithm with ML through MATLAB ML Toolbox

